

Figure 1

### Coordination Interfaces

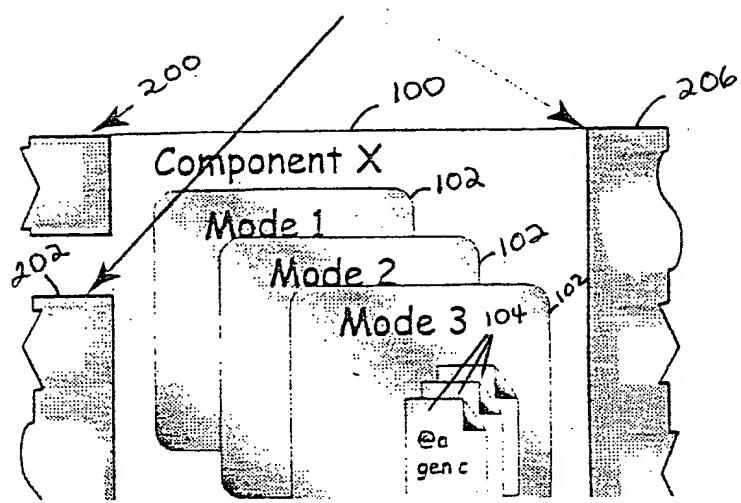
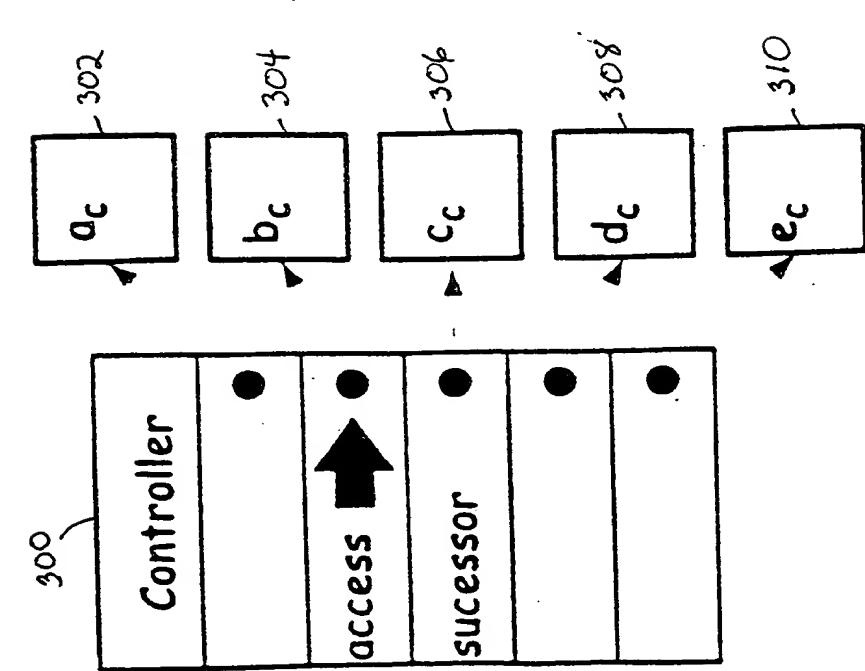


Figure 2



Token successor

Token (access right)

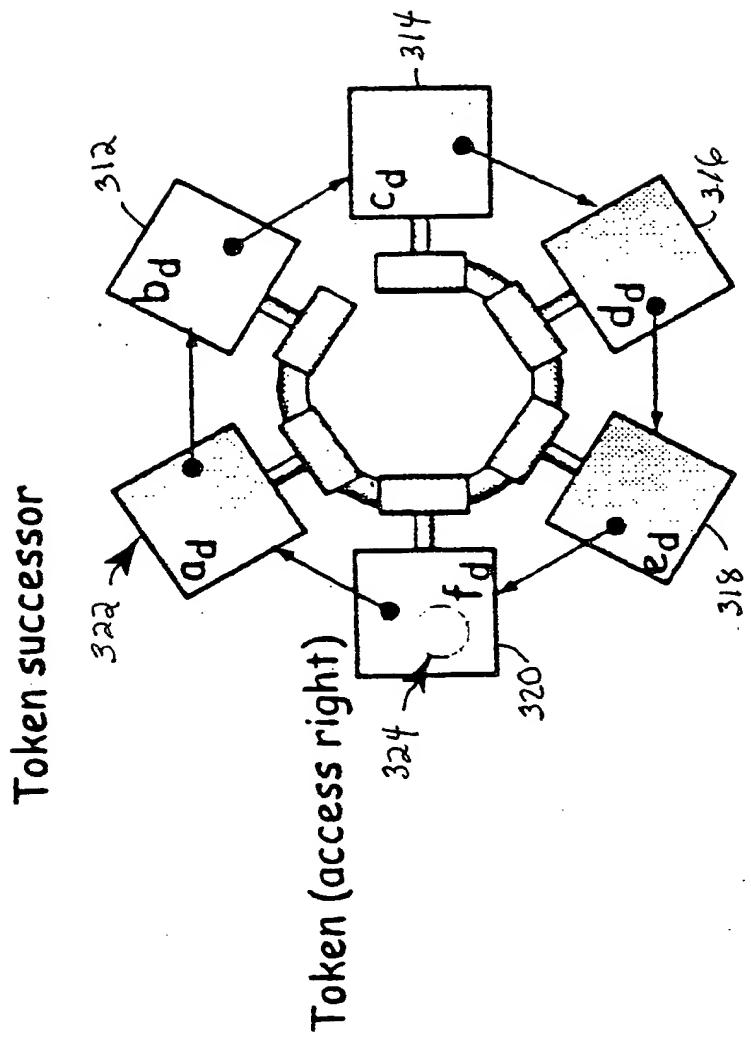


Figure 3A

Figure 3B

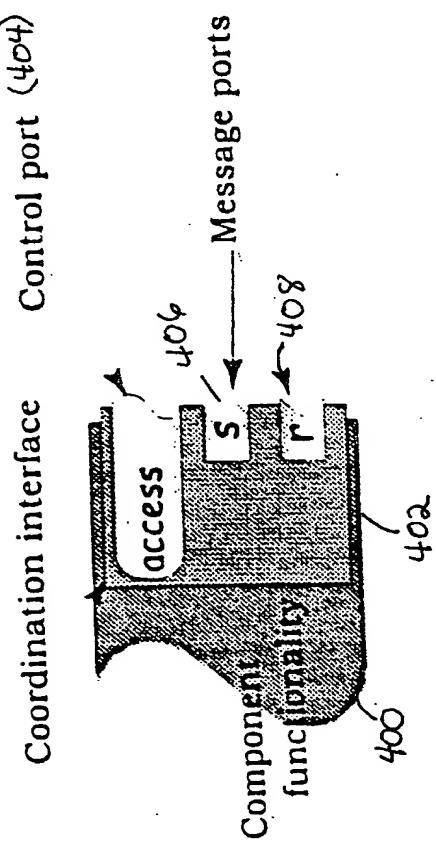


Figure 4A

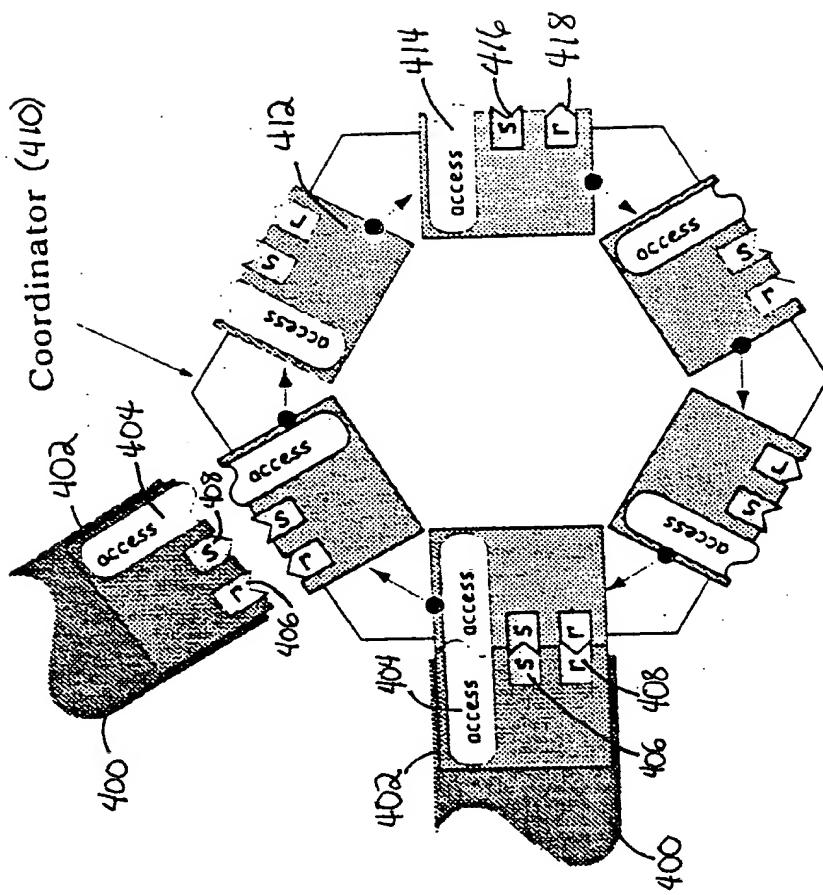


Figure 4B

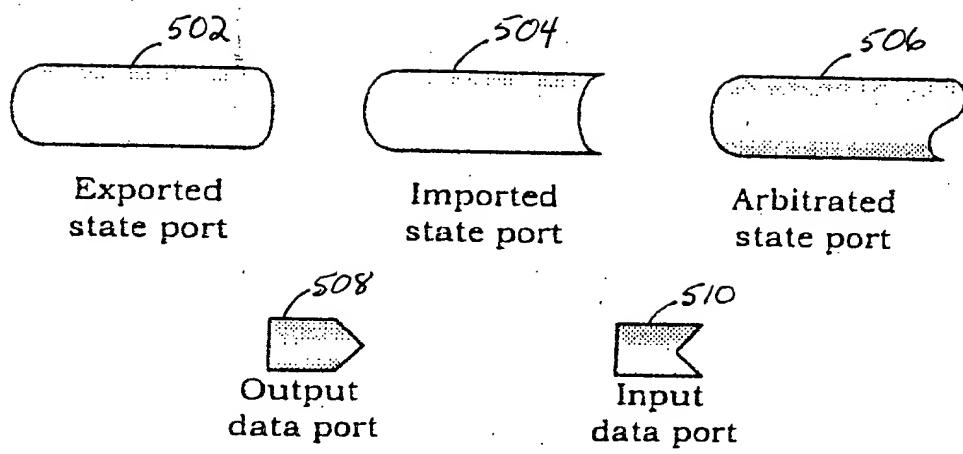


Figure 5

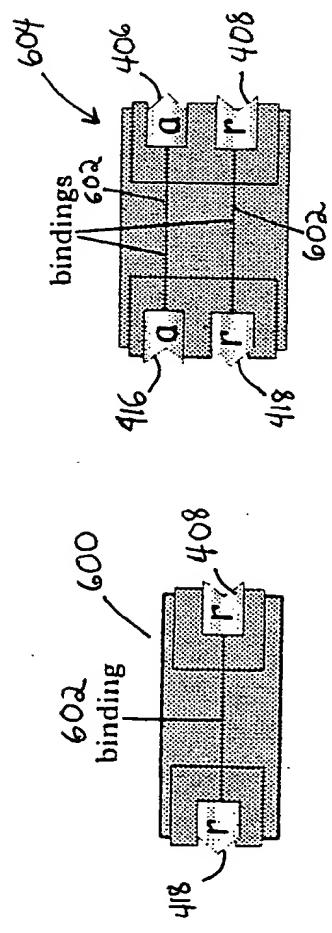


Figure 6A

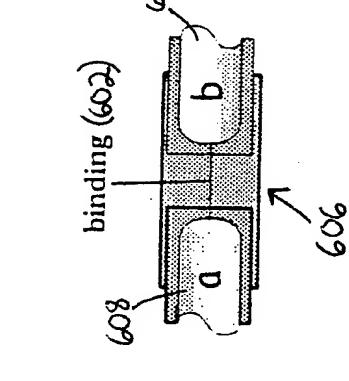


Figure 6B

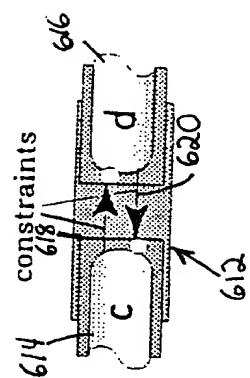


Figure 6C

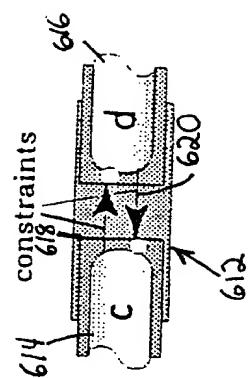


Figure 6D

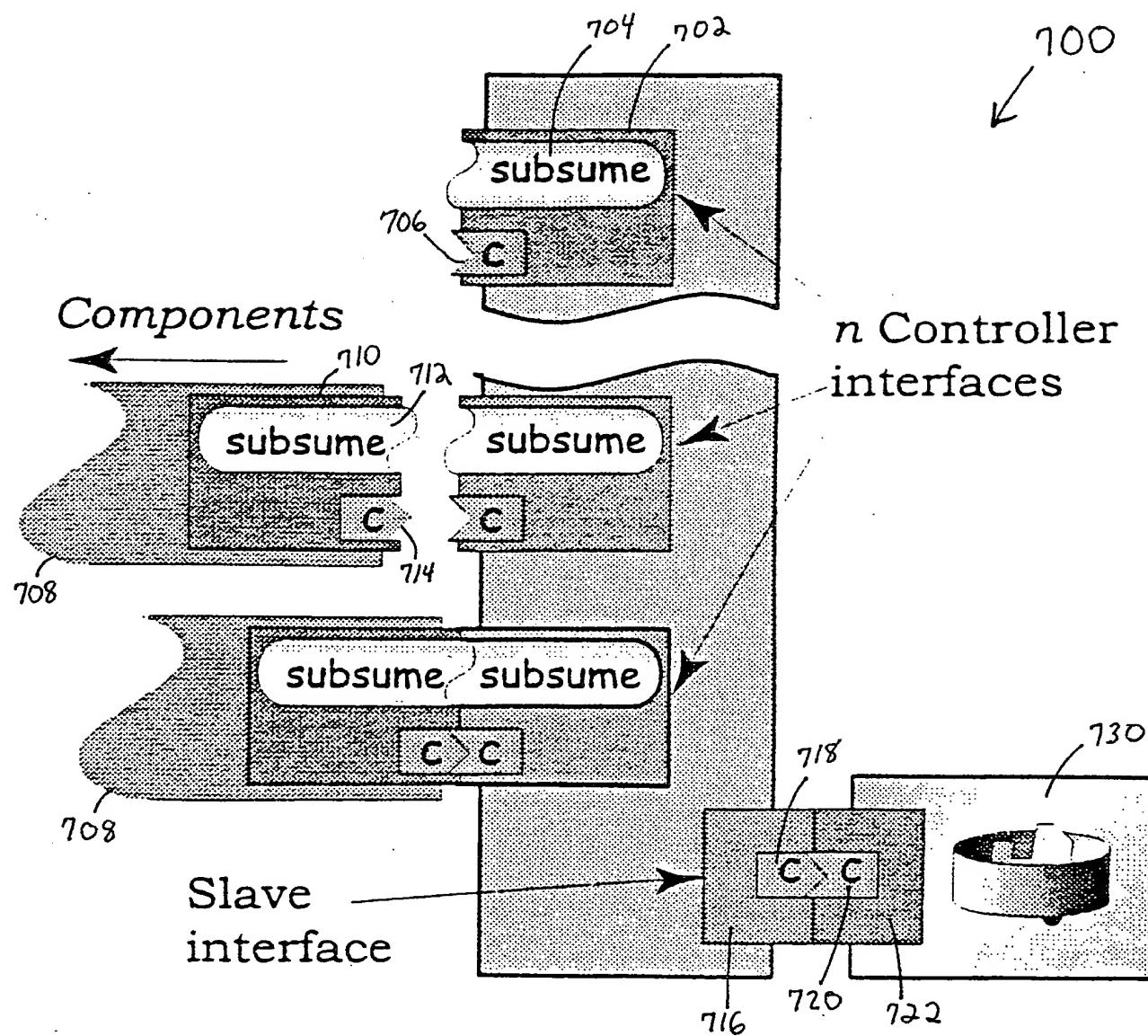


Figure 7

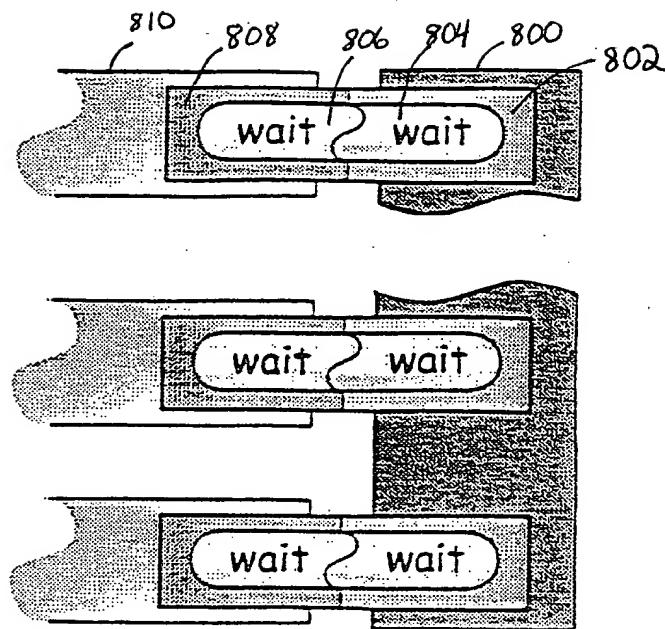


Figure 8

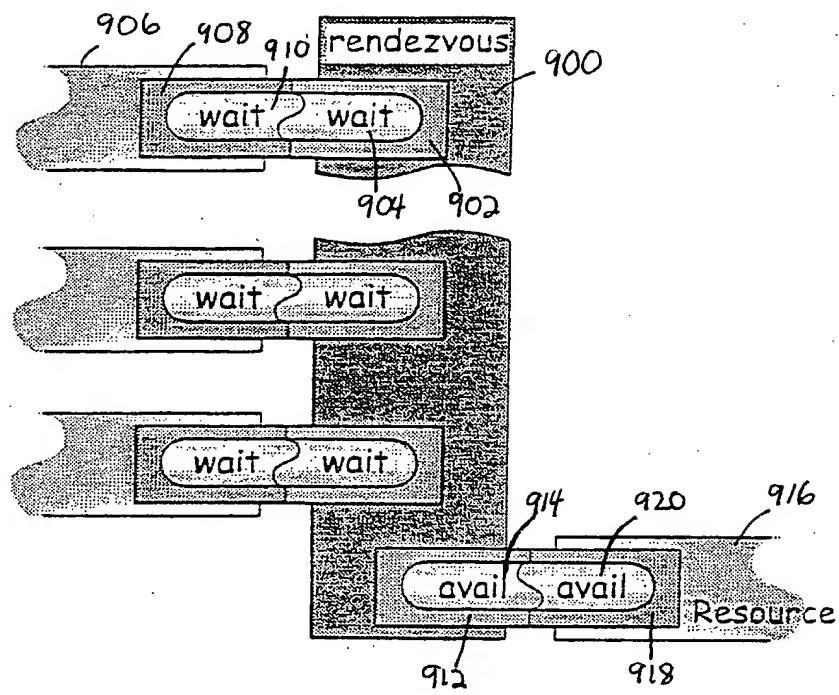


Figure 9

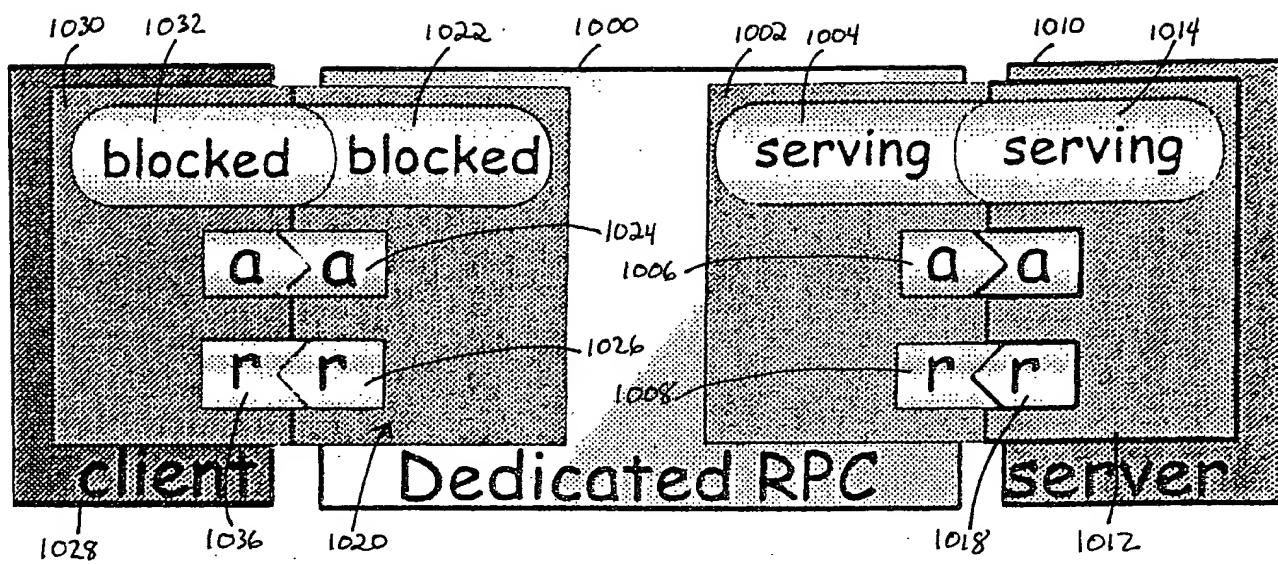


Figure 10

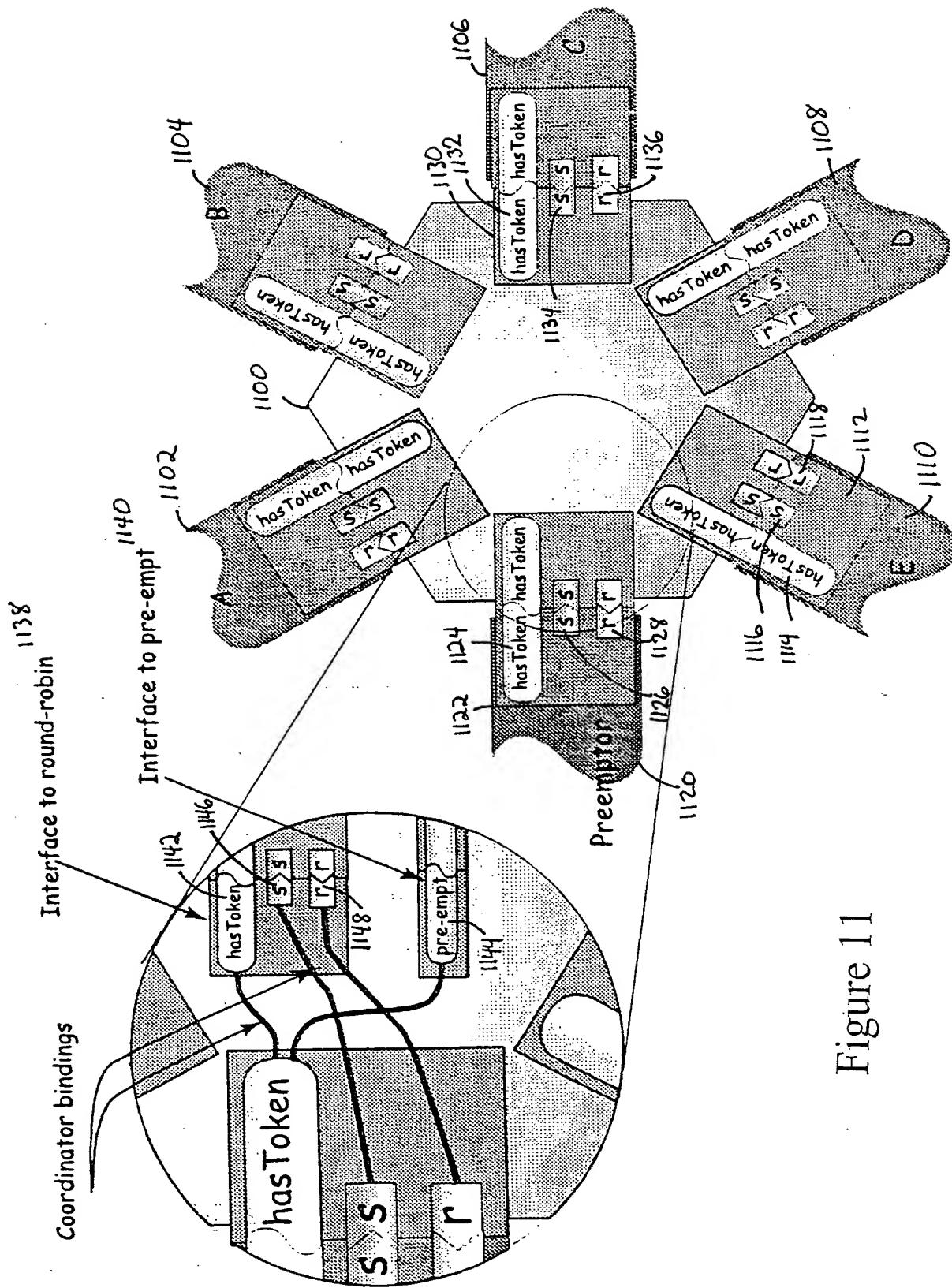


Figure 11

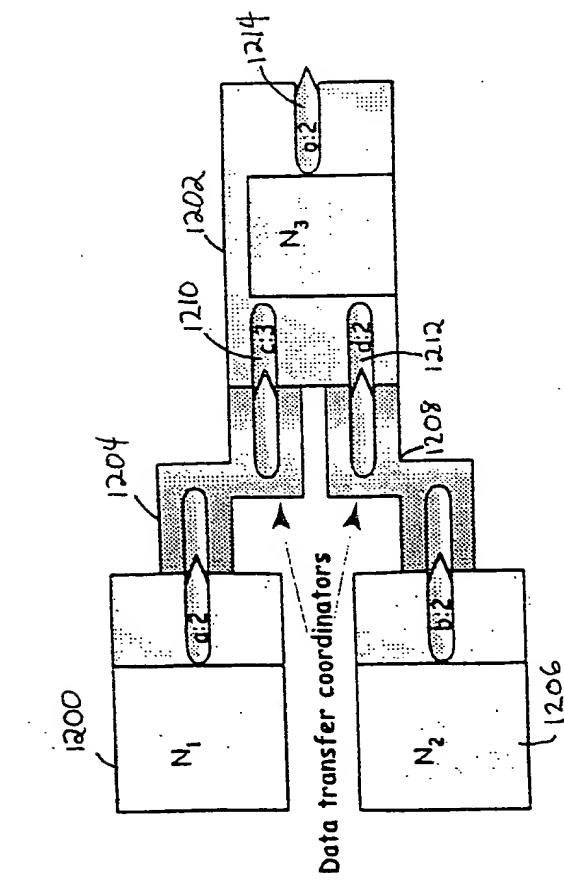


Figure 12A

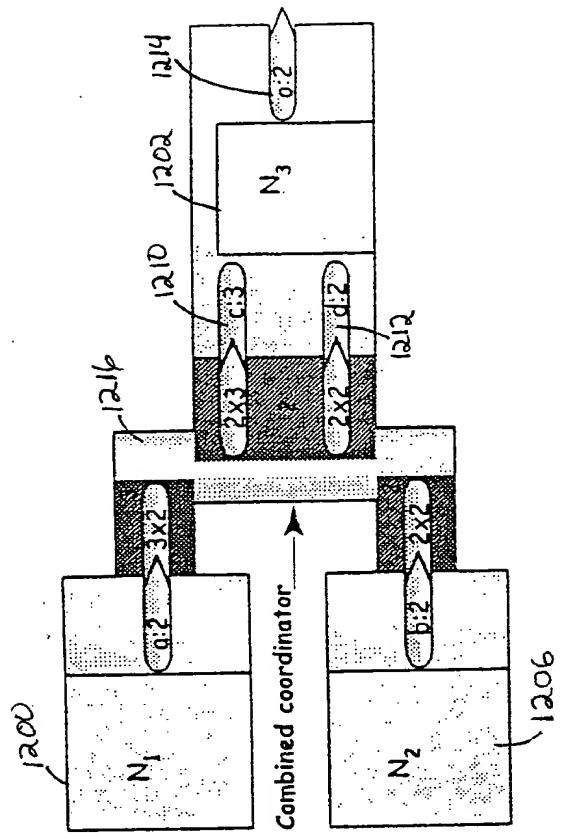


Figure 12B

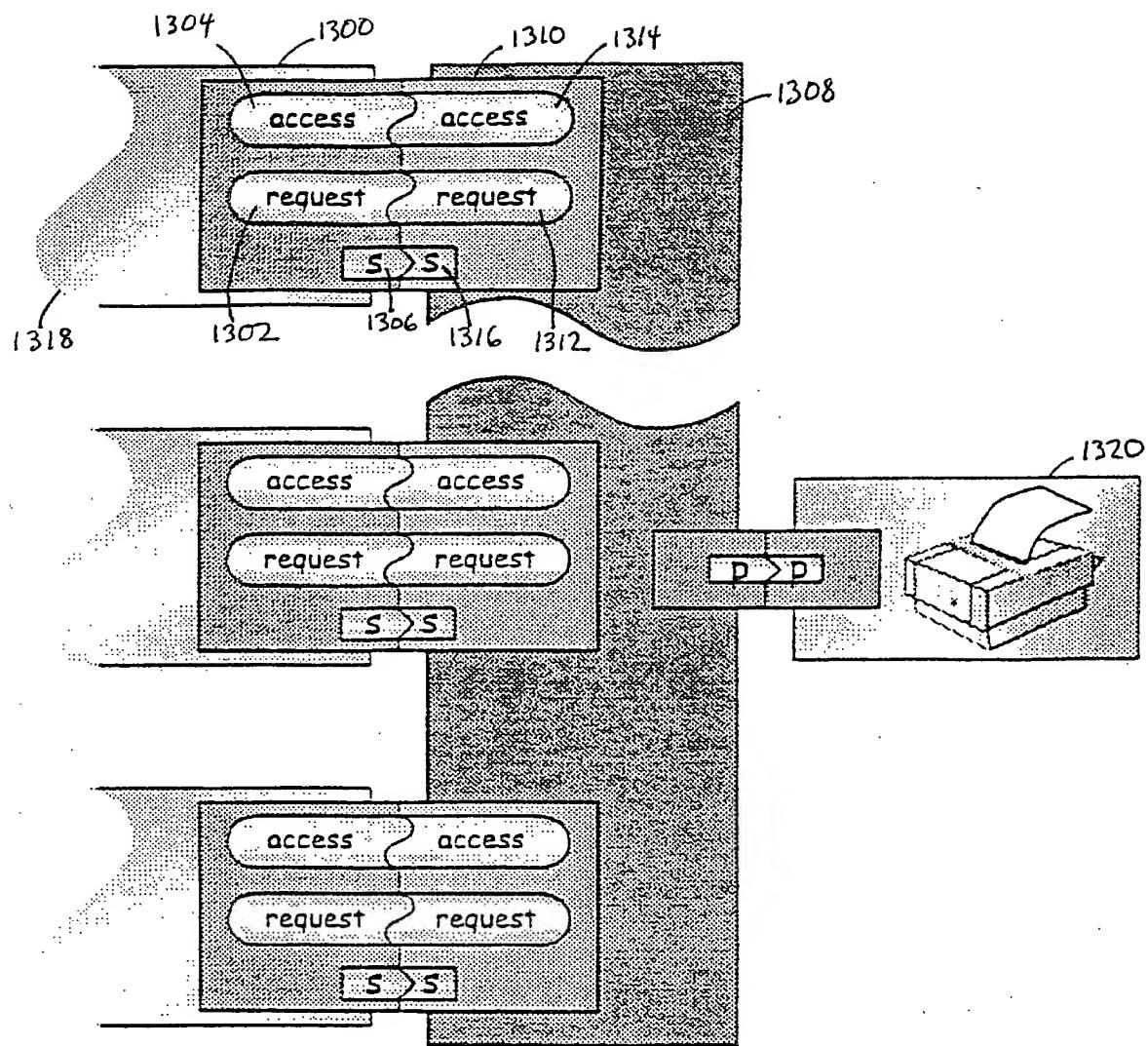


Figure 13

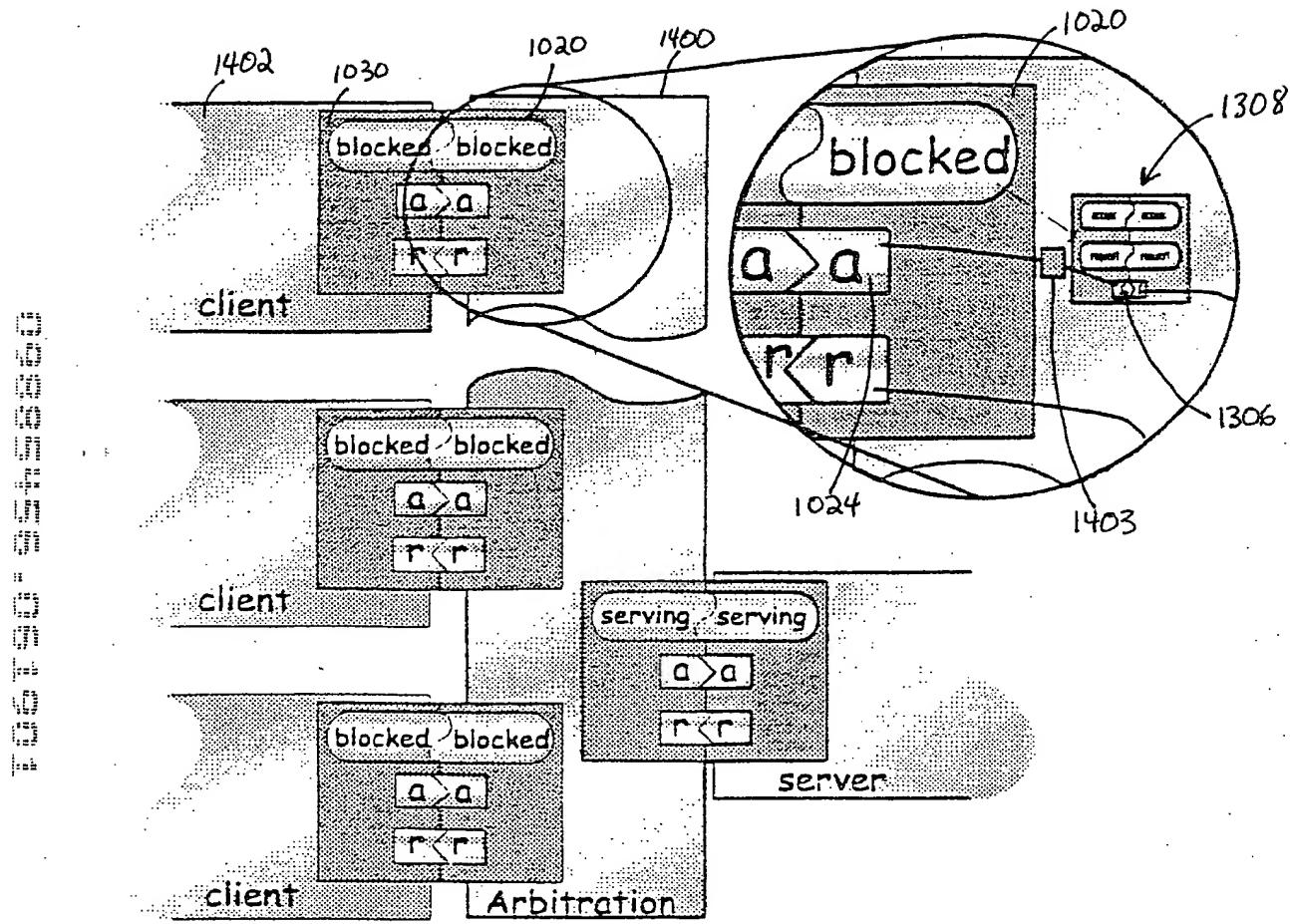


Figure 14

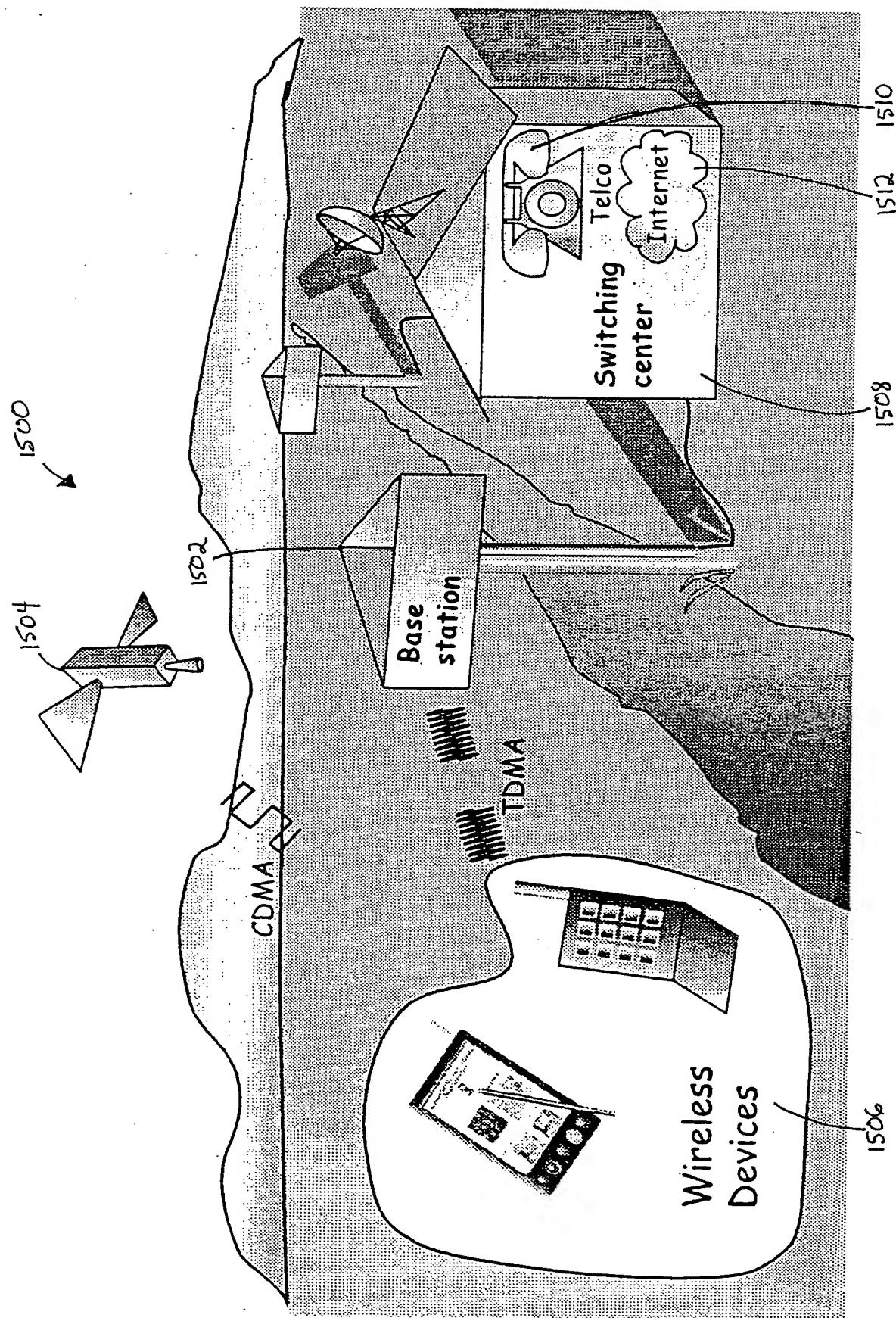


Figure 15

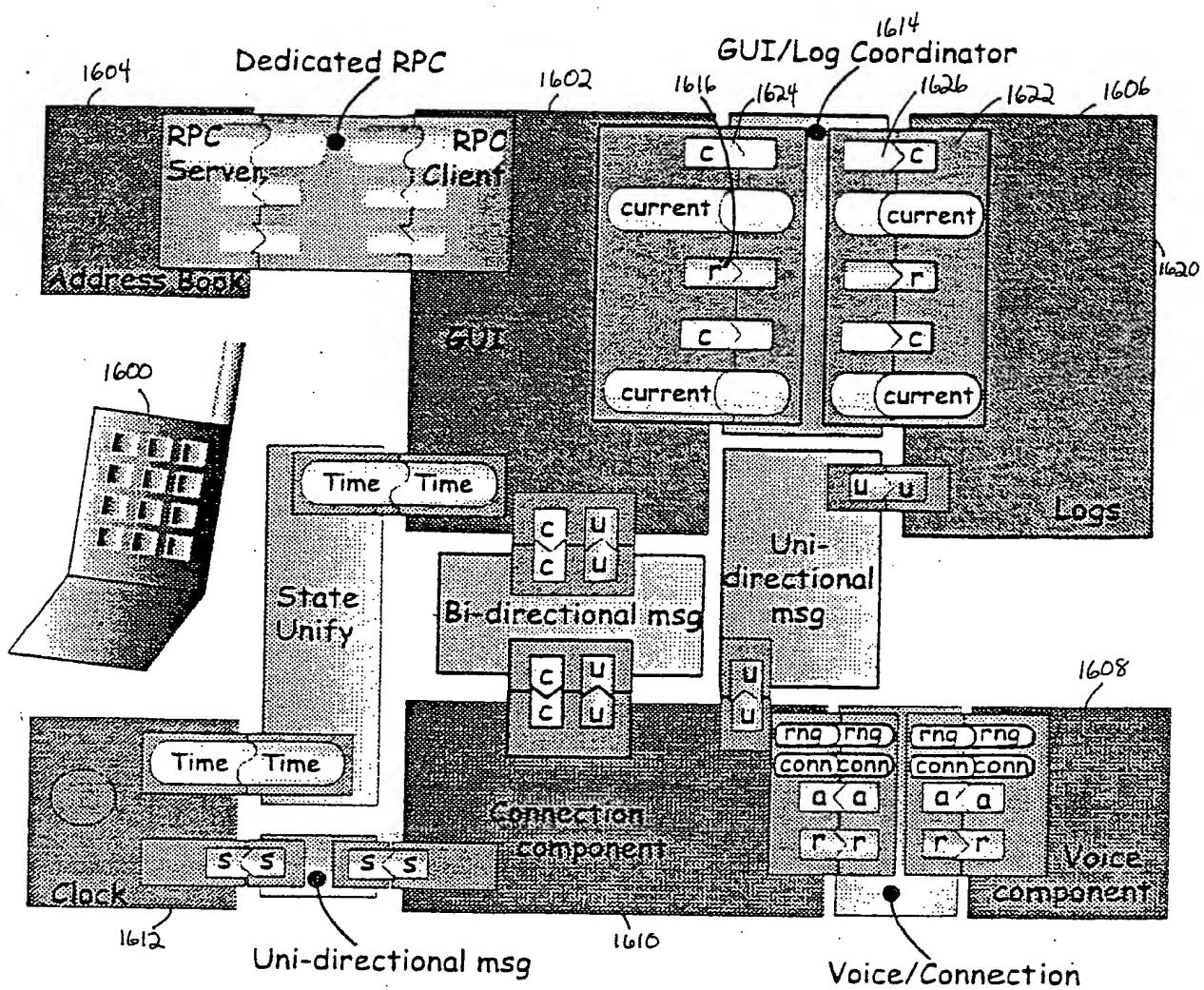


Figure 16

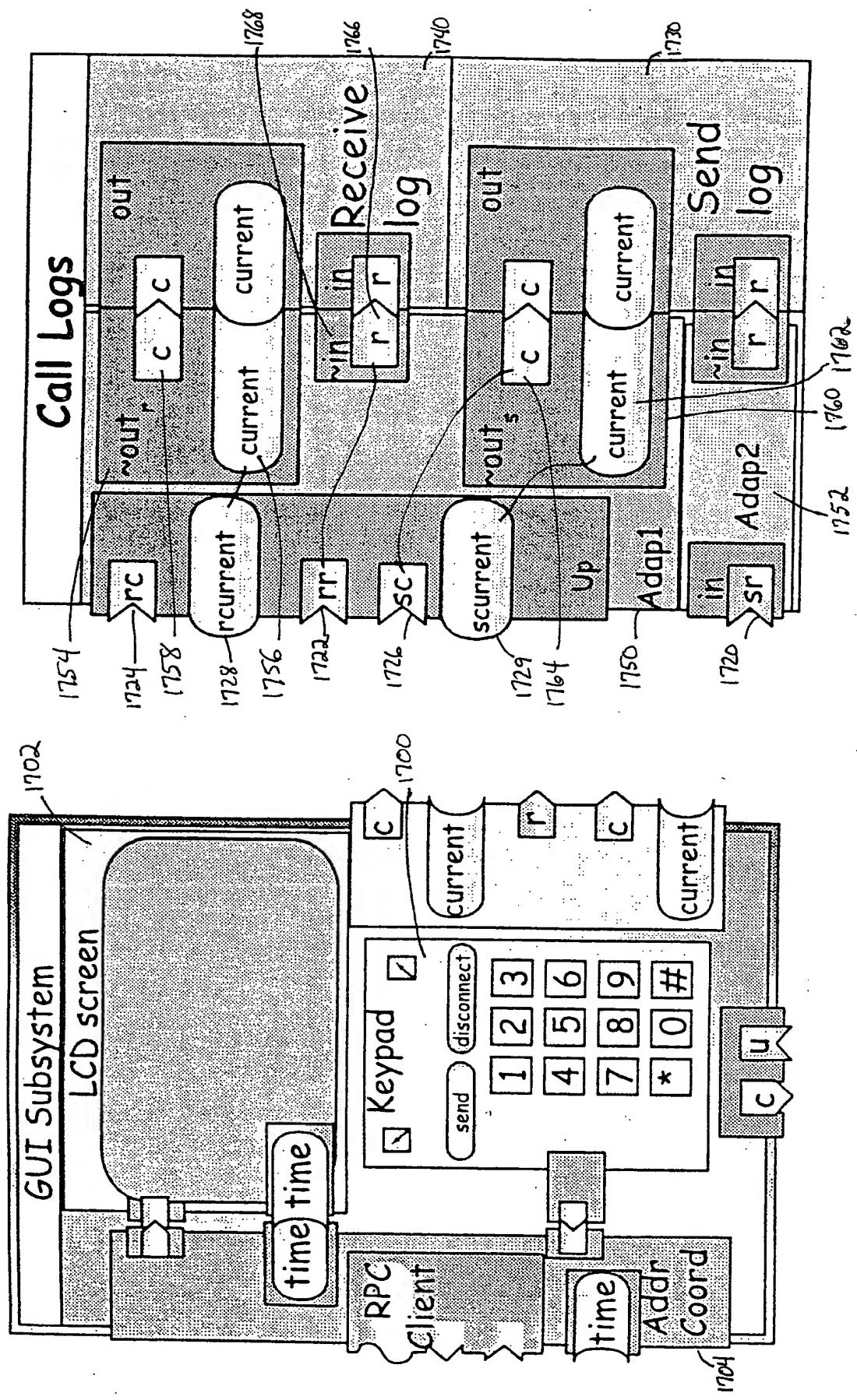


Figure 17A

Figure 17B

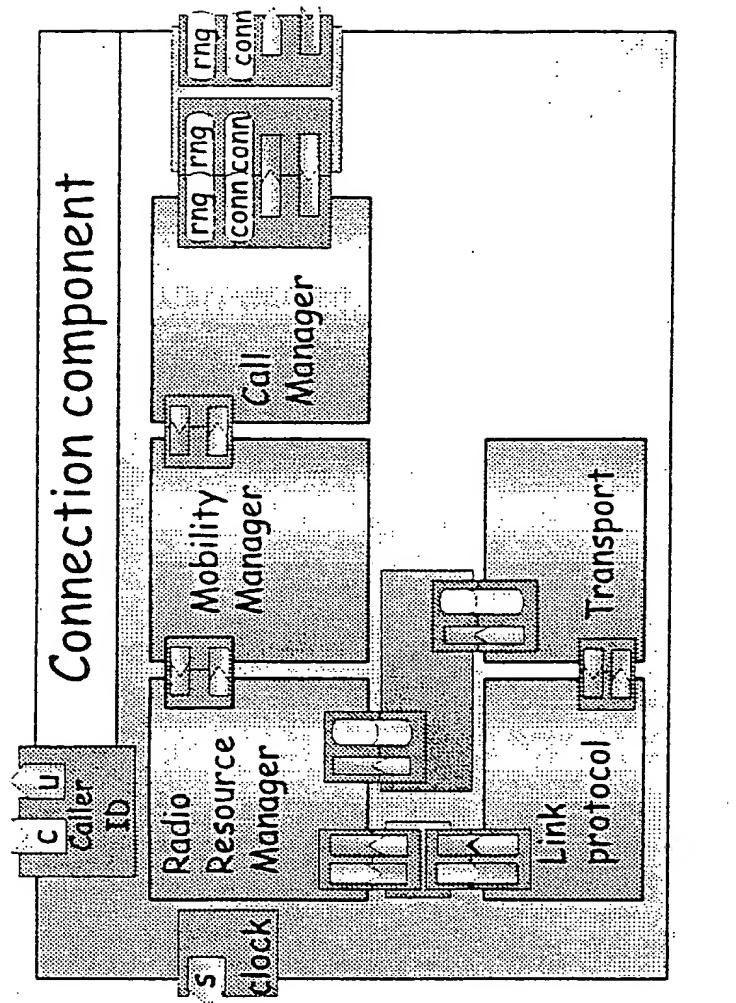


Figure 18B

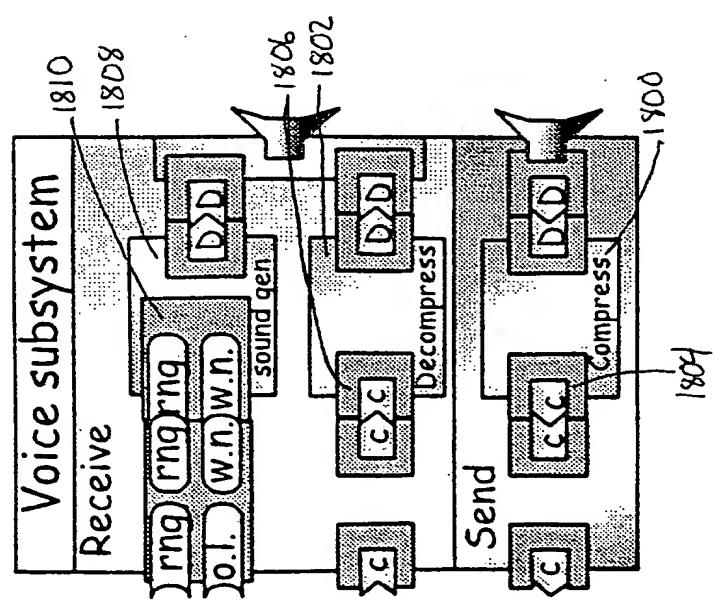


Figure 18A

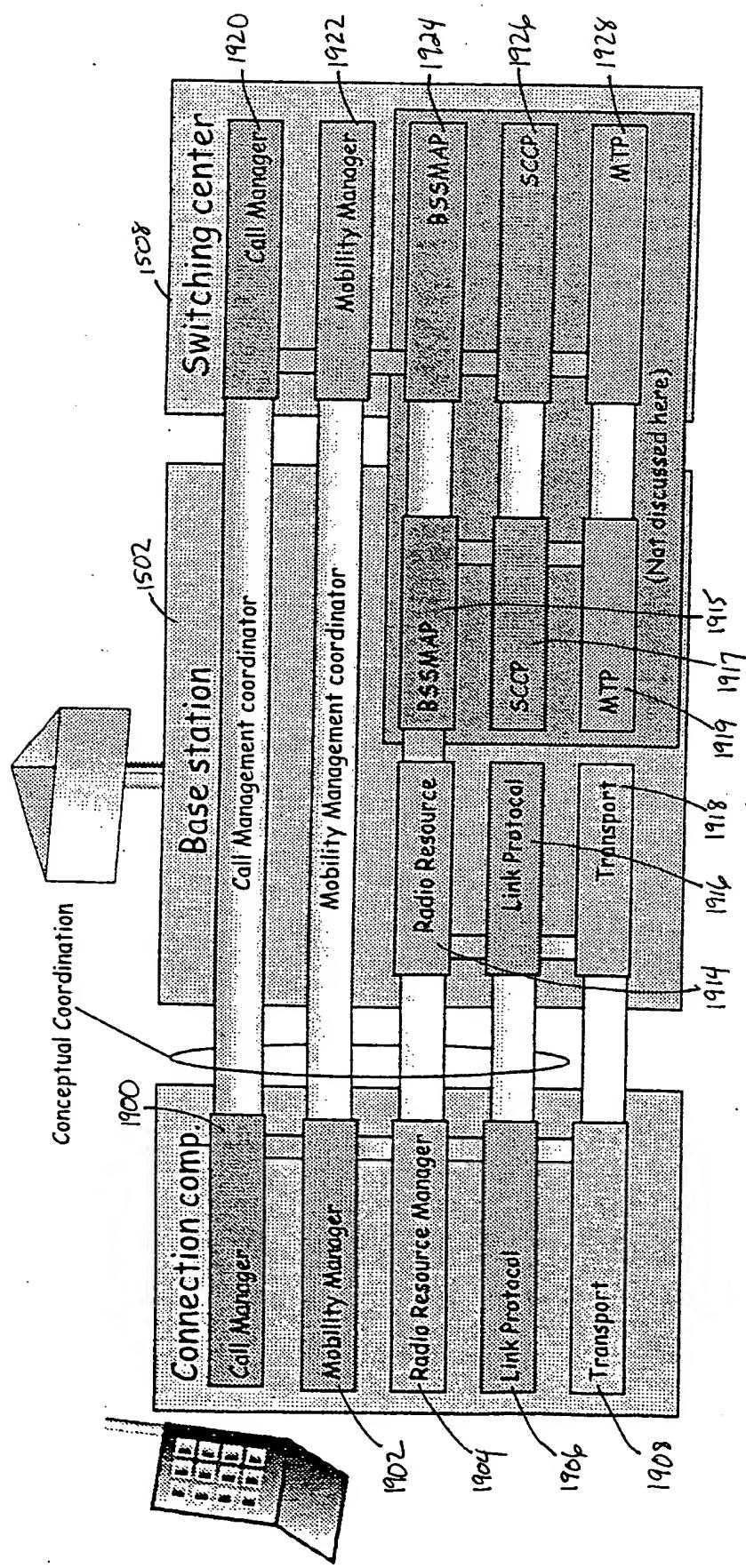


Figure 19

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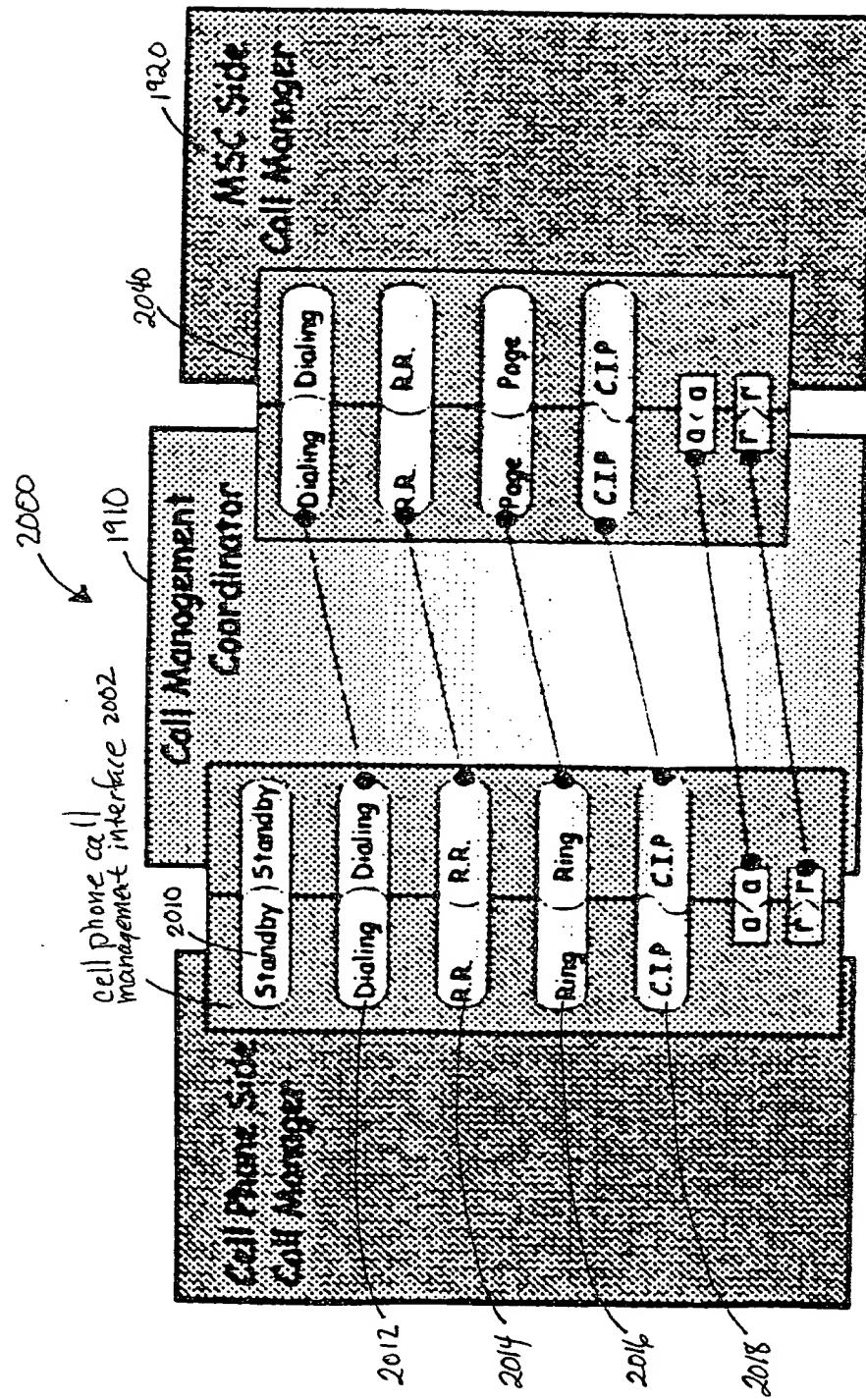


Figure 20

2100 2102 2104 2106 2108 2110 2112 2114 2116 2118 2120

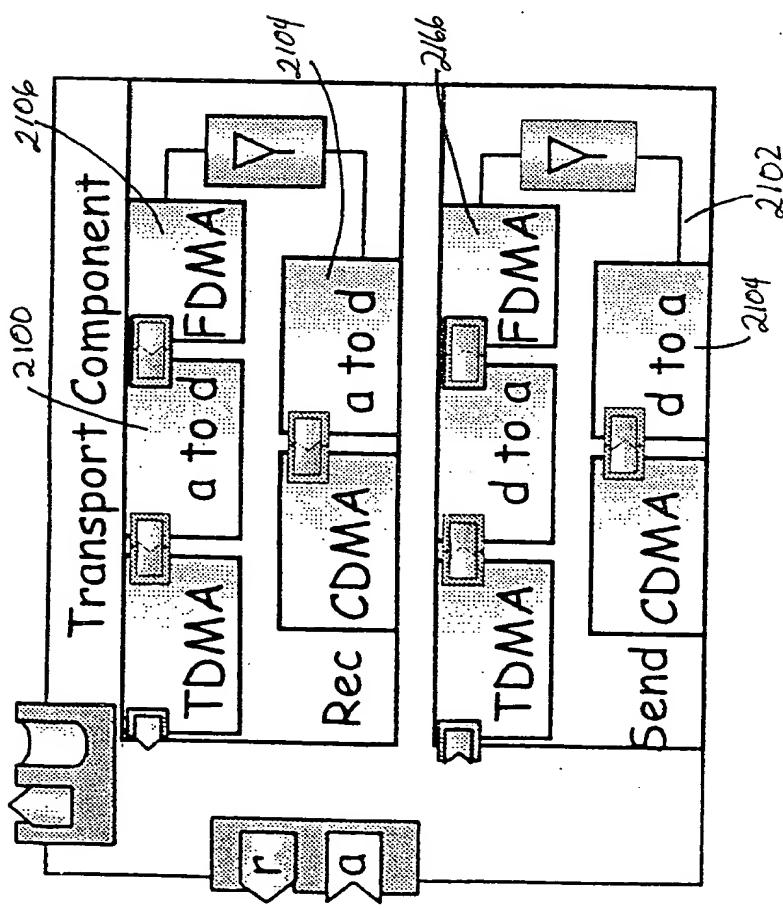


Figure 21A

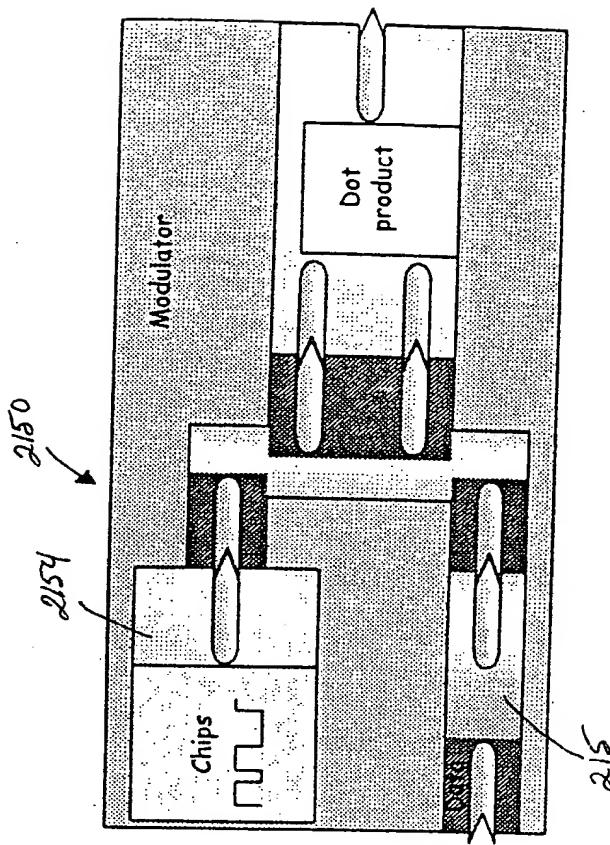


Figure 21B

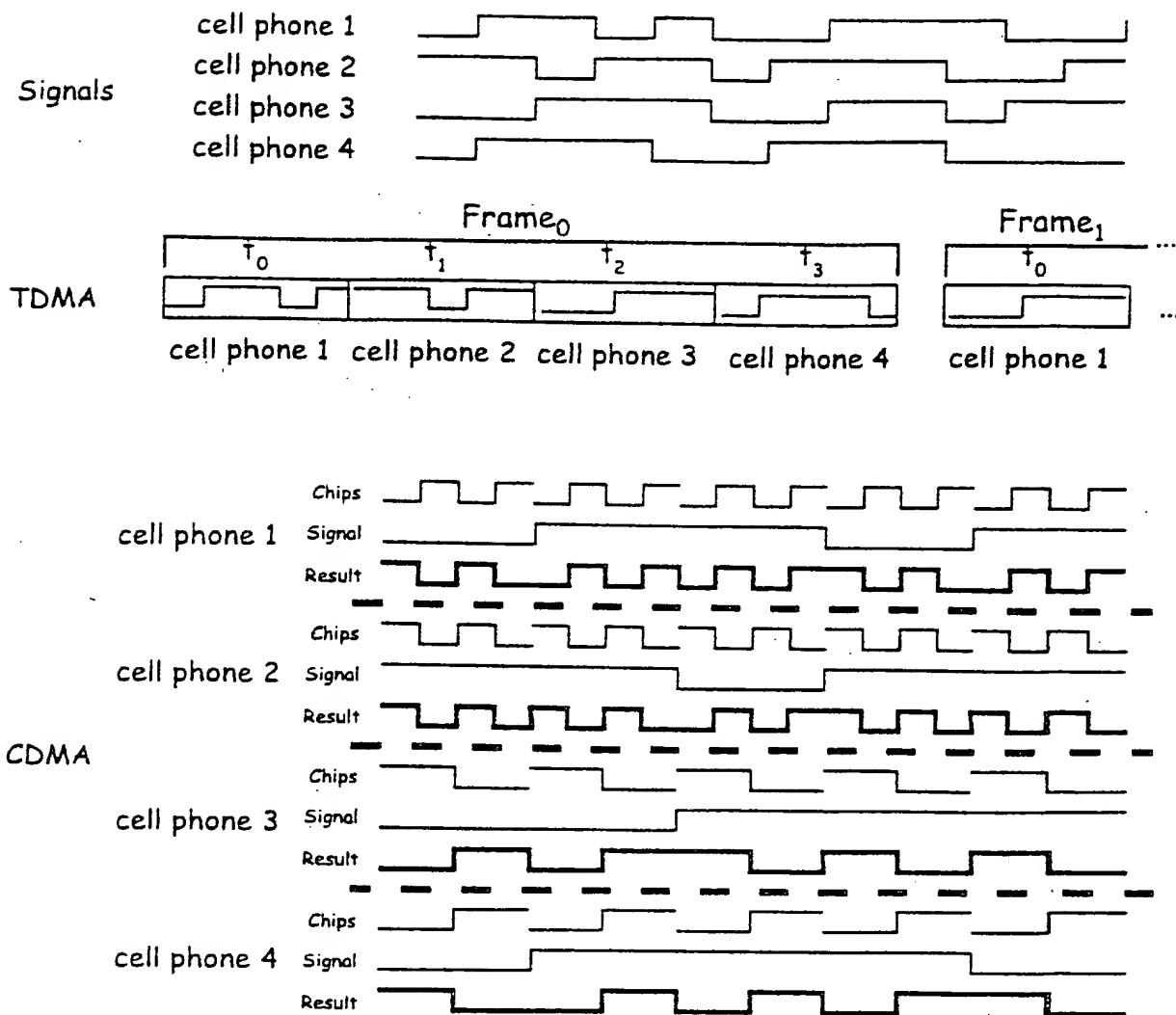


Figure 22

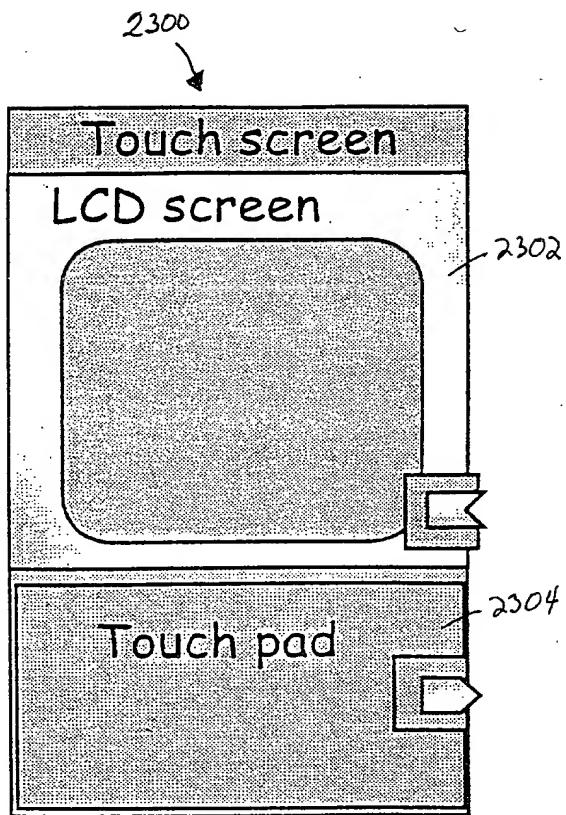


Figure 23A

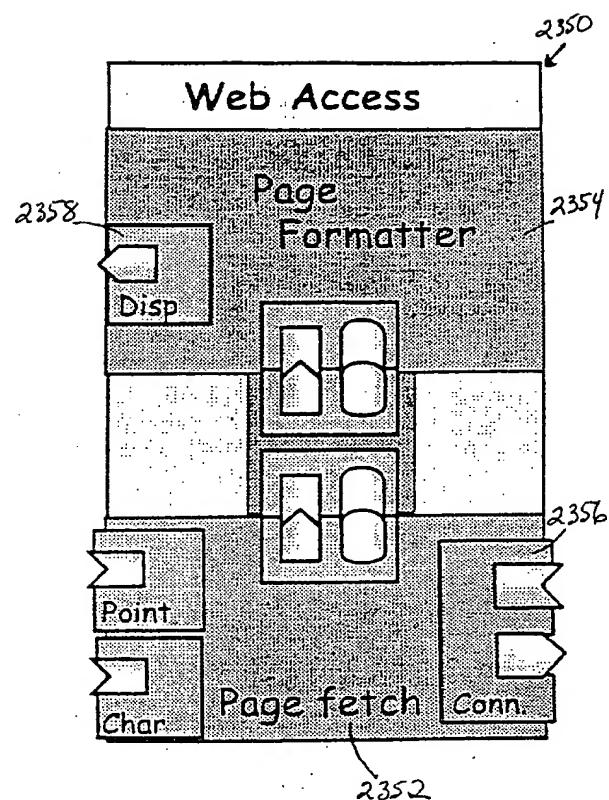


Figure 23B

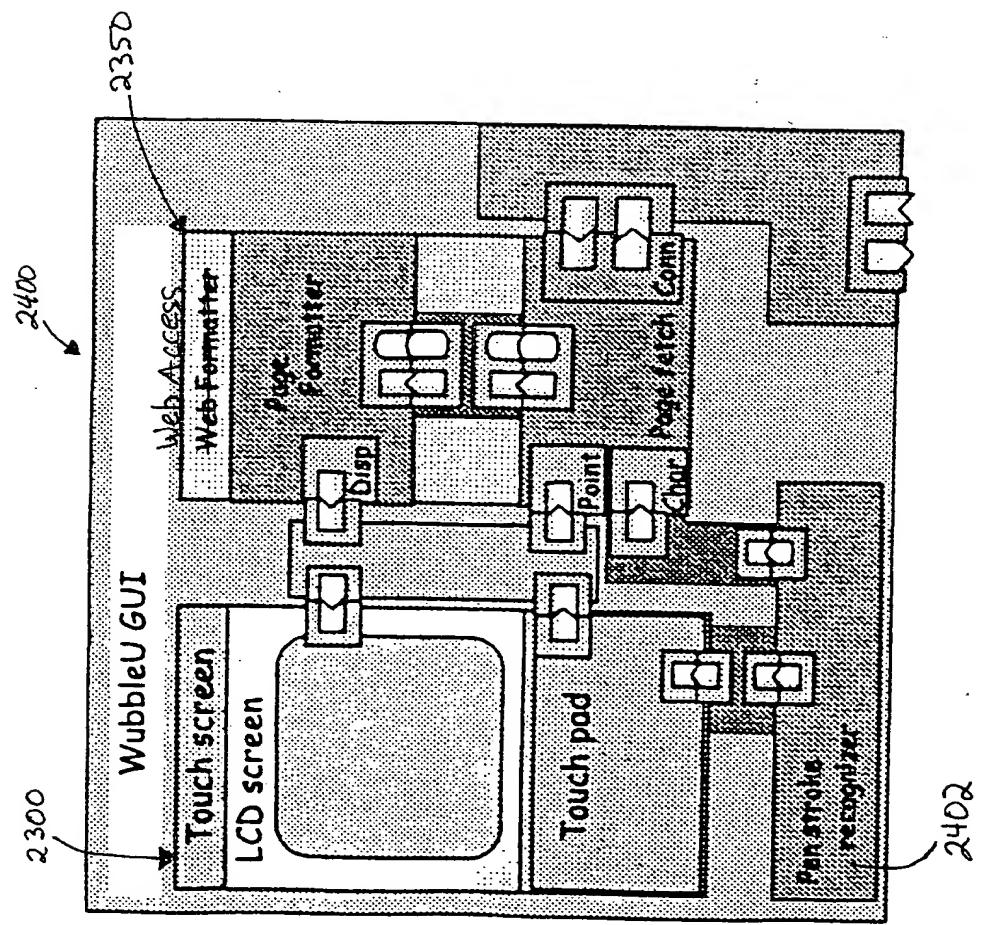


Figure 24A

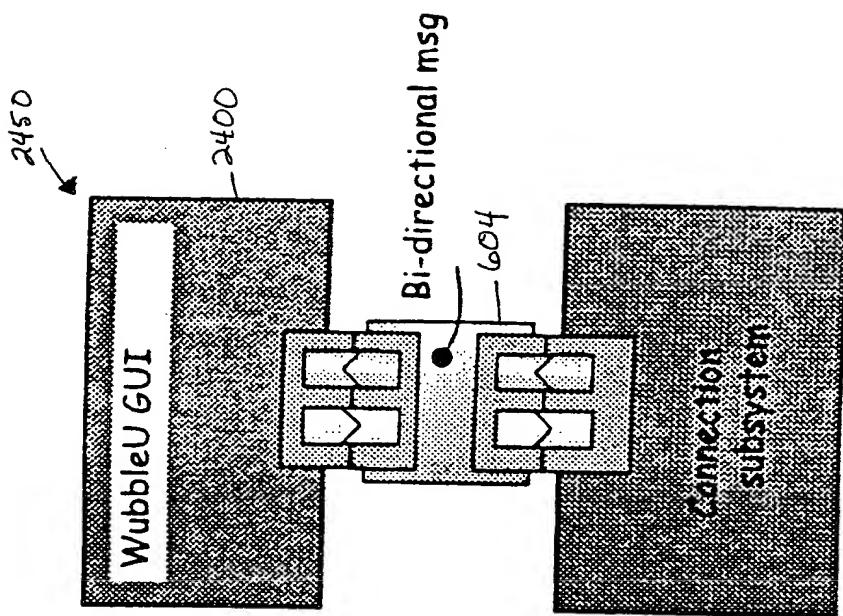


Figure 24B

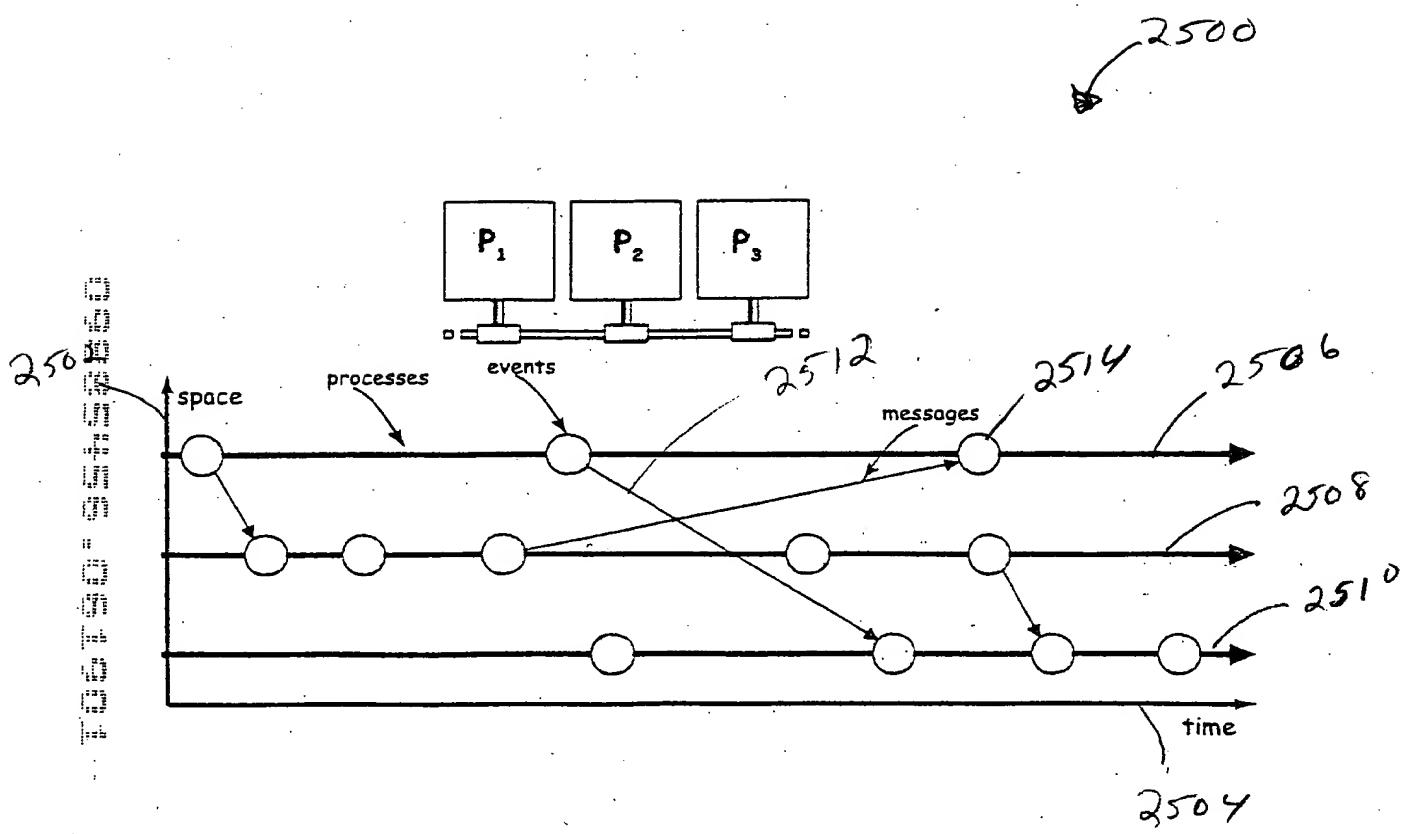


Figure 25

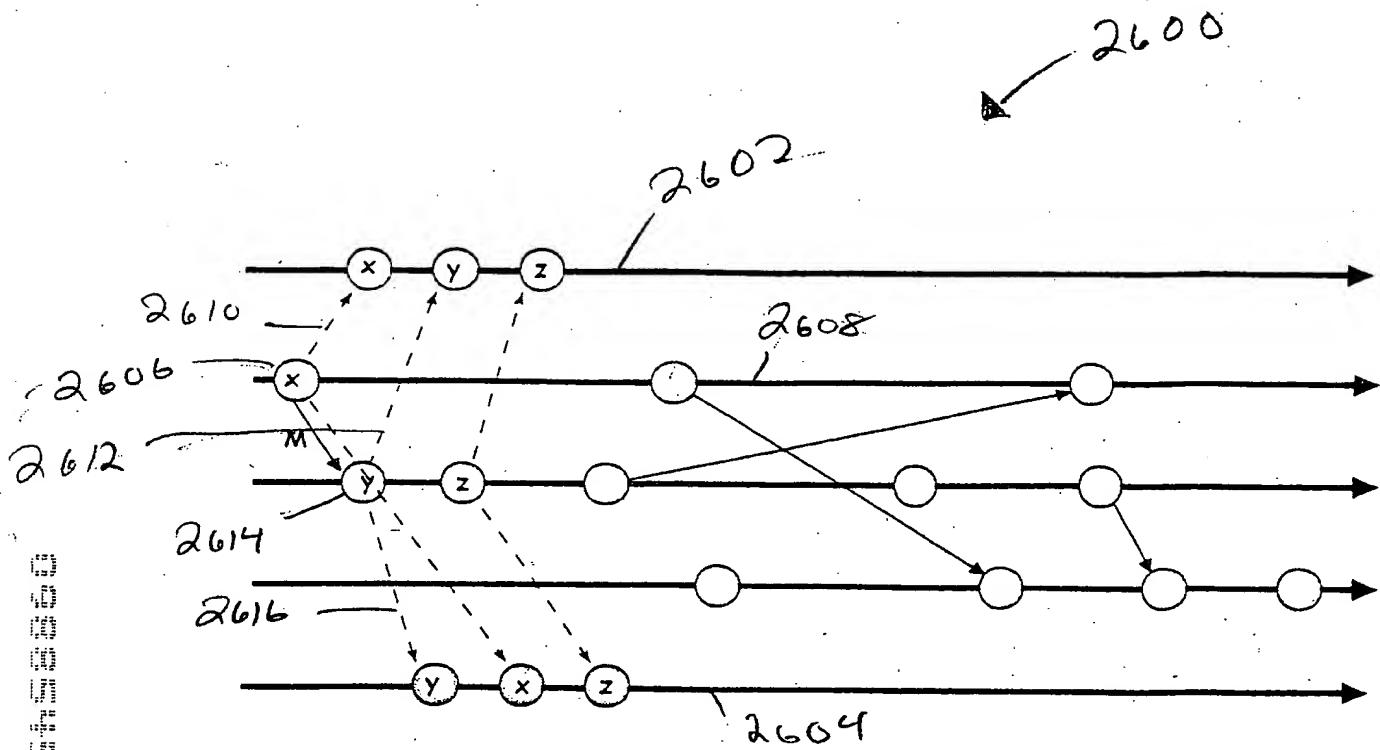


Figure 26

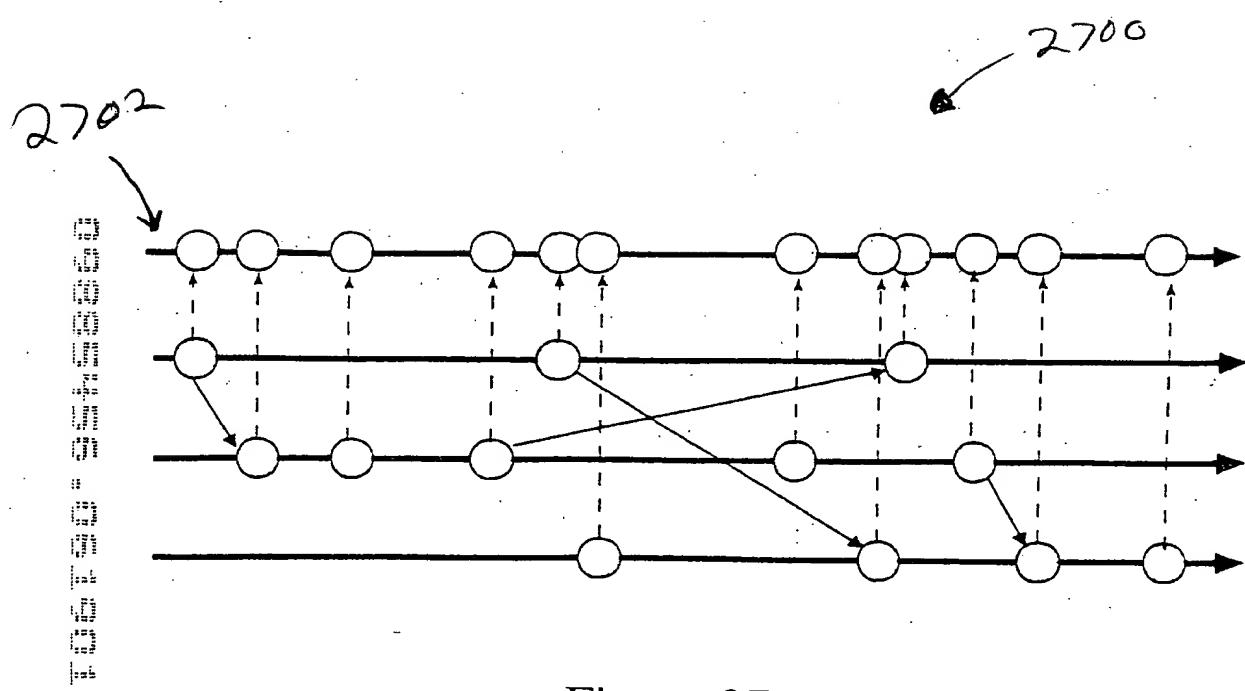


Figure 27

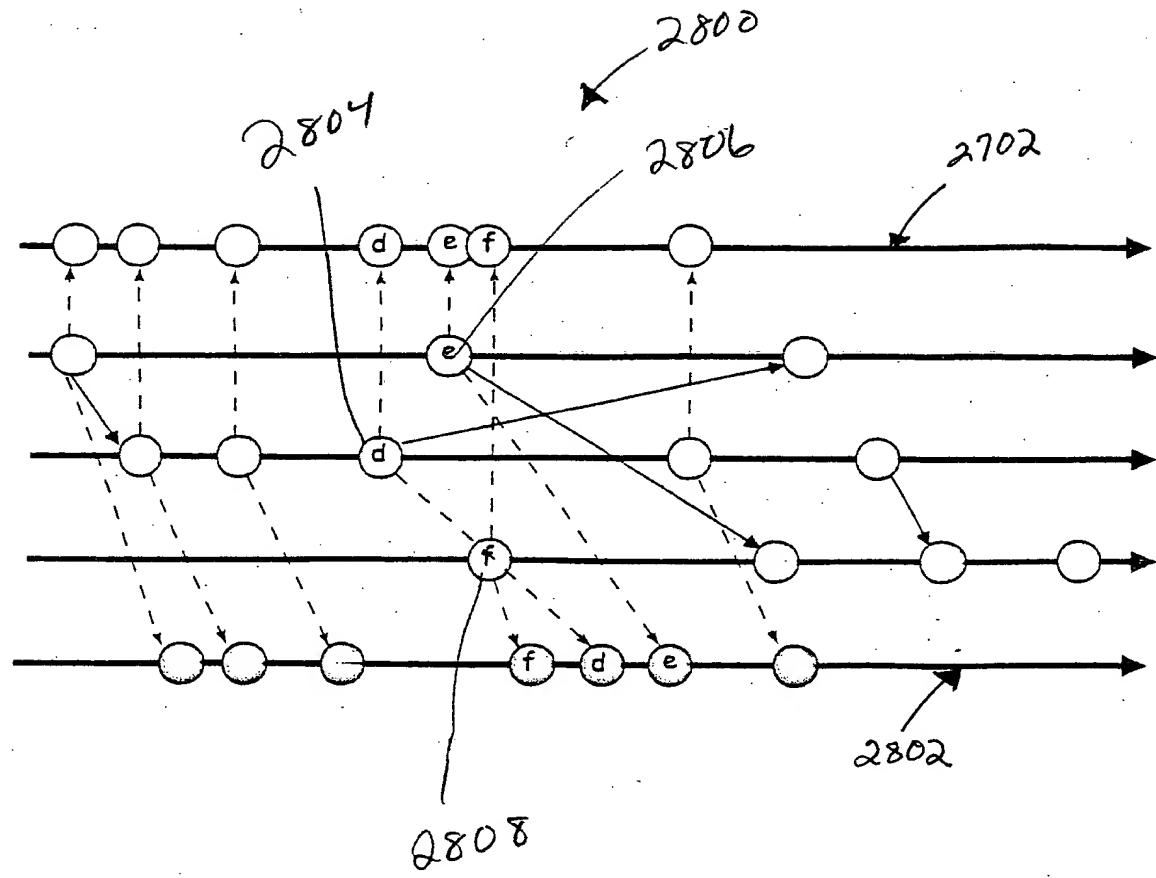


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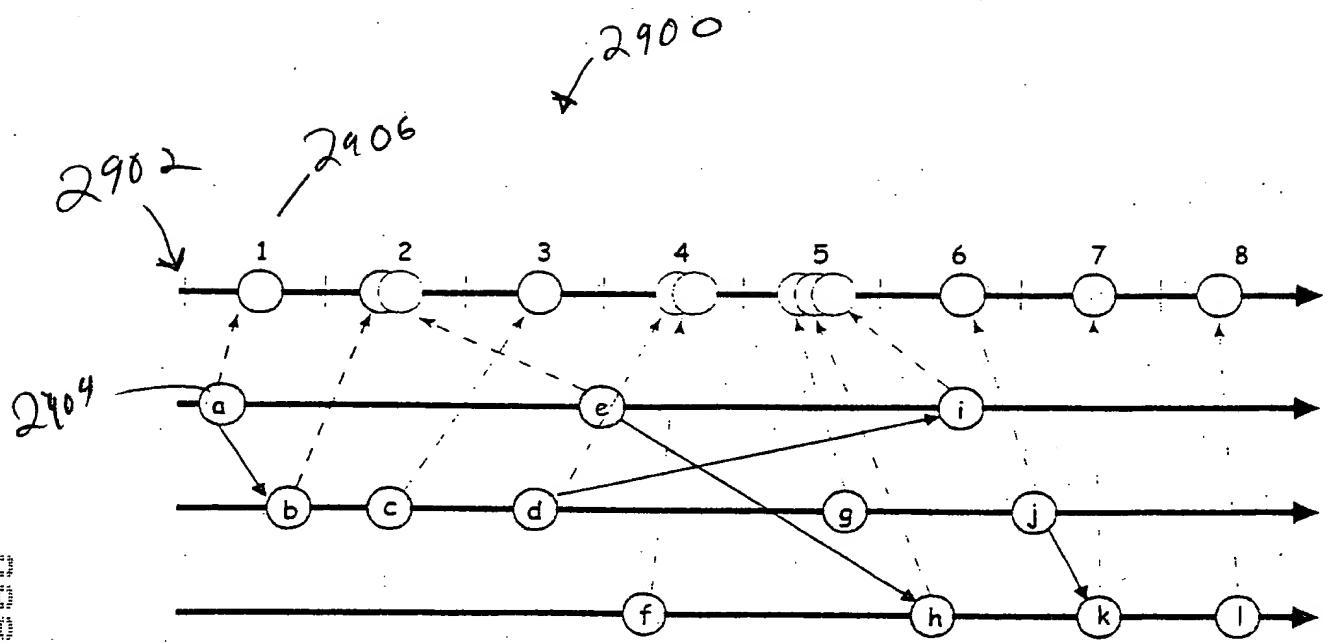


Figure 29

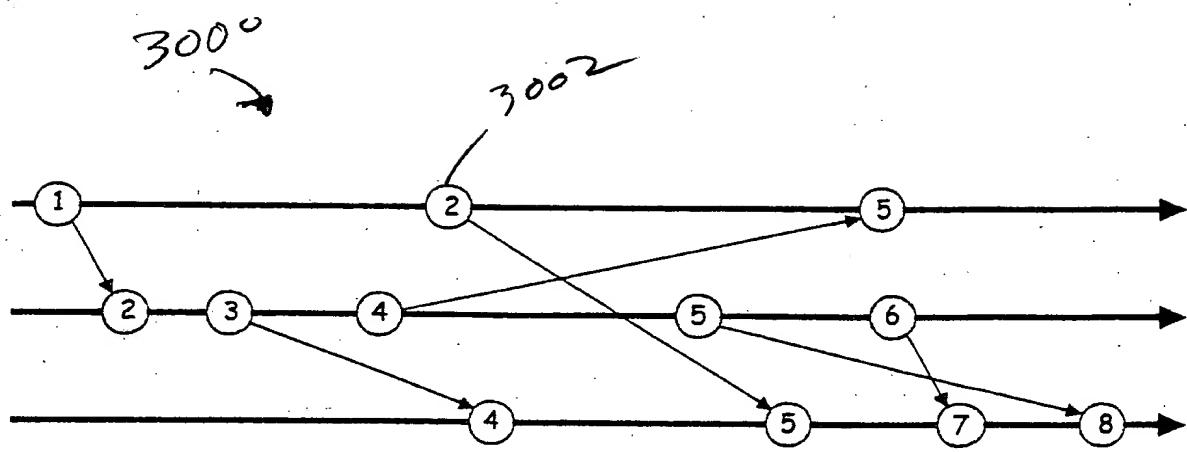


Figure 30

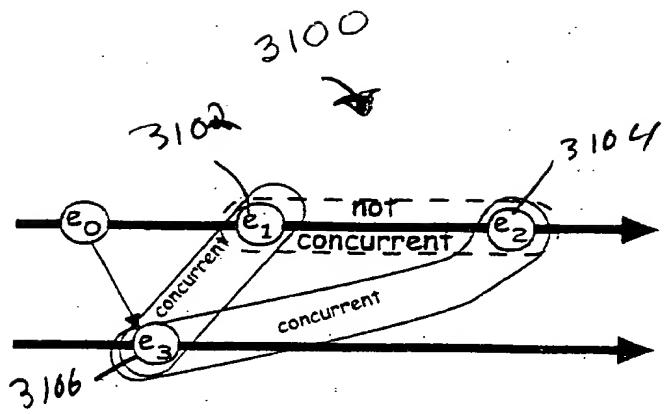


Figure 31

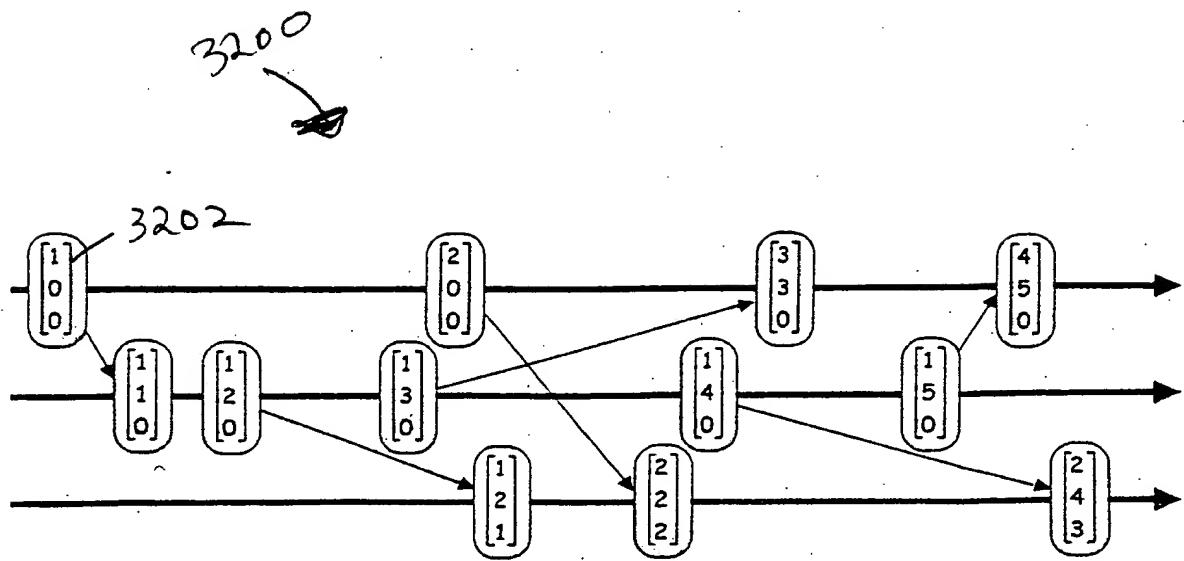


Figure 32

3300

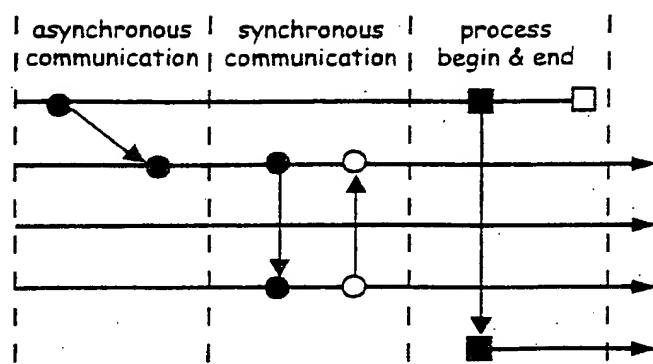


Figure 33

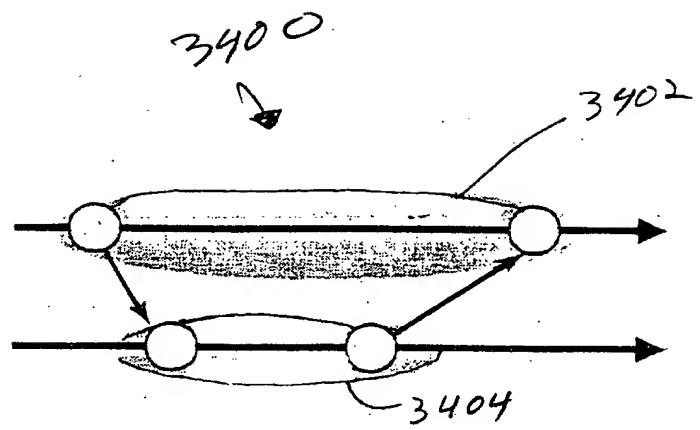


Figure 34

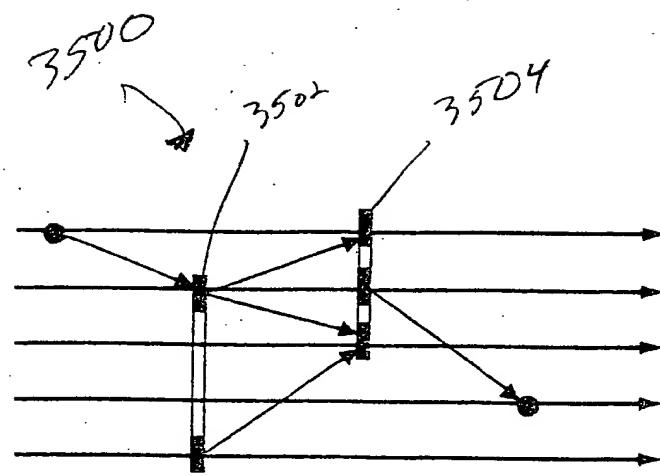


Figure 35

3600  
3602  
3604  
3606  
3608

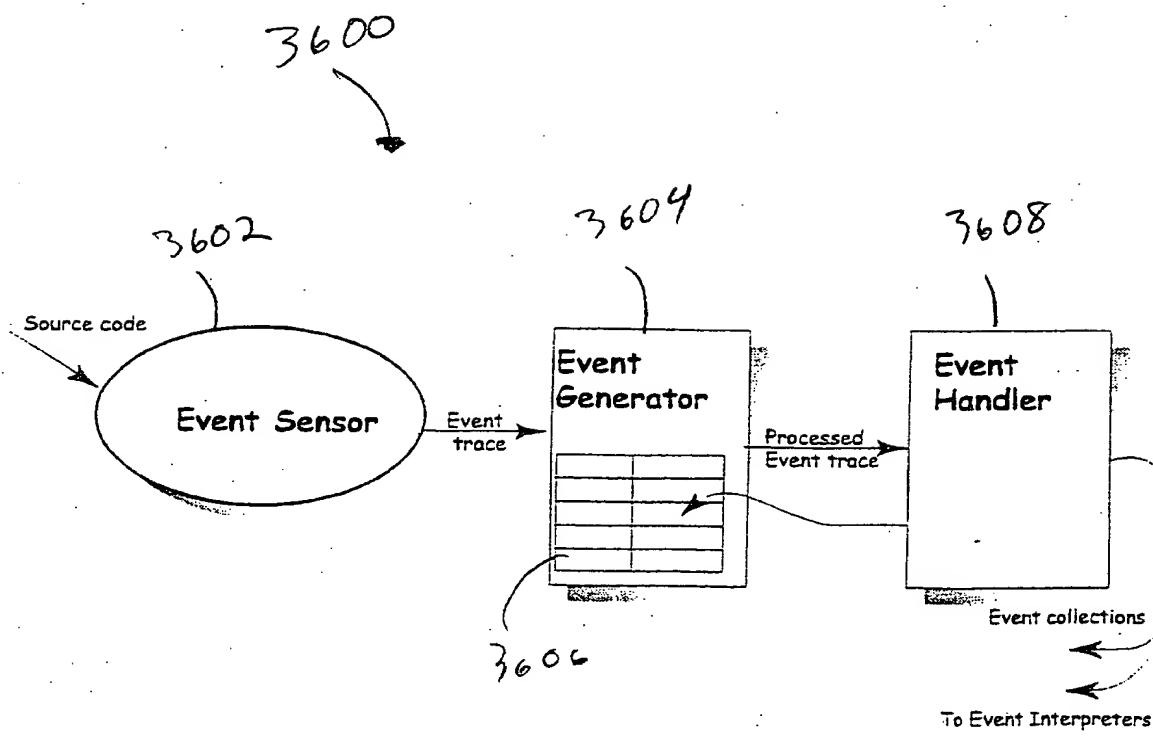


Figure 36

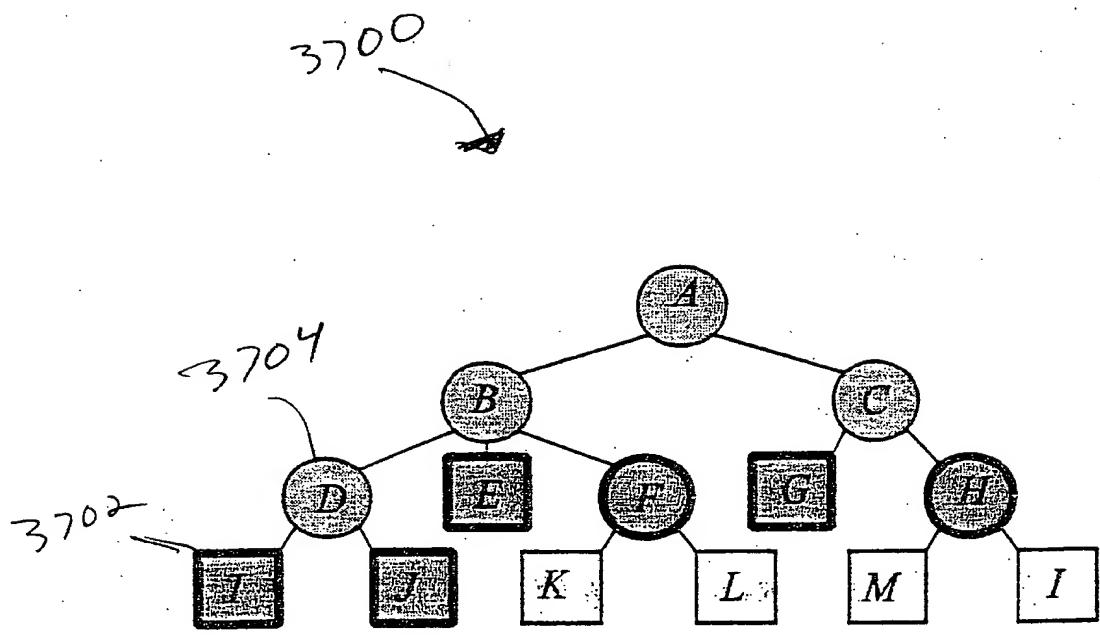


Figure 37

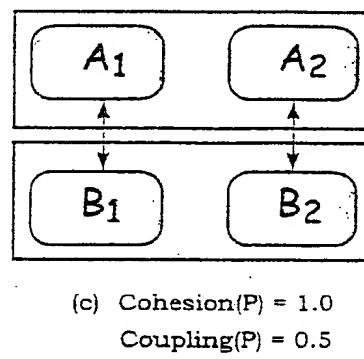
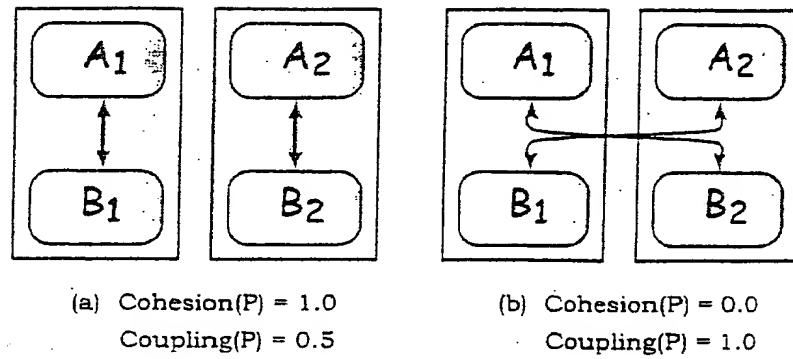


Figure 38

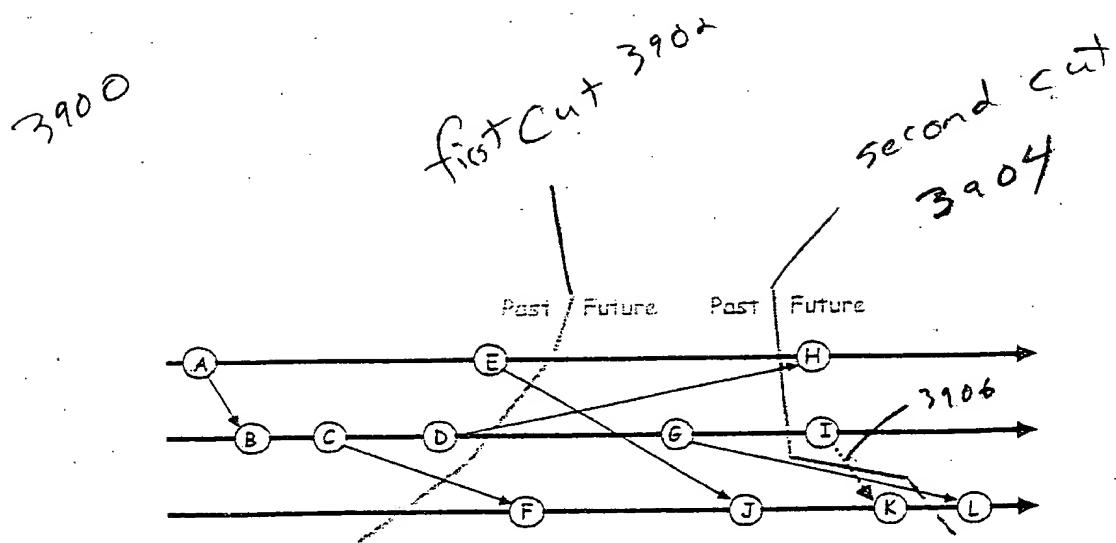


Figure 39

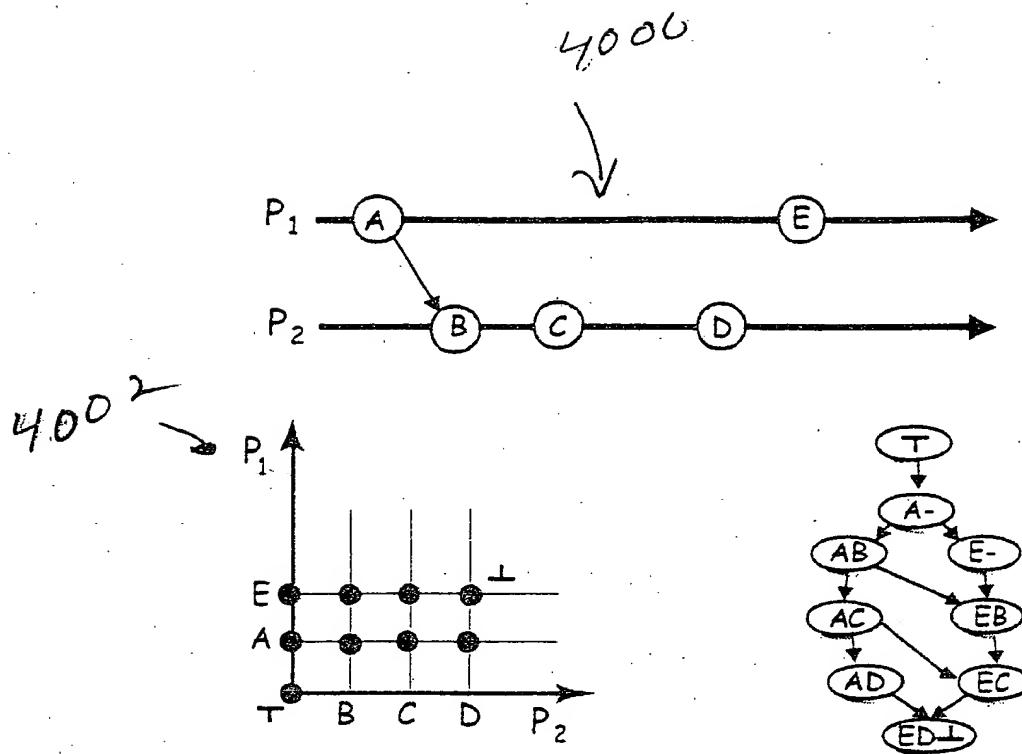
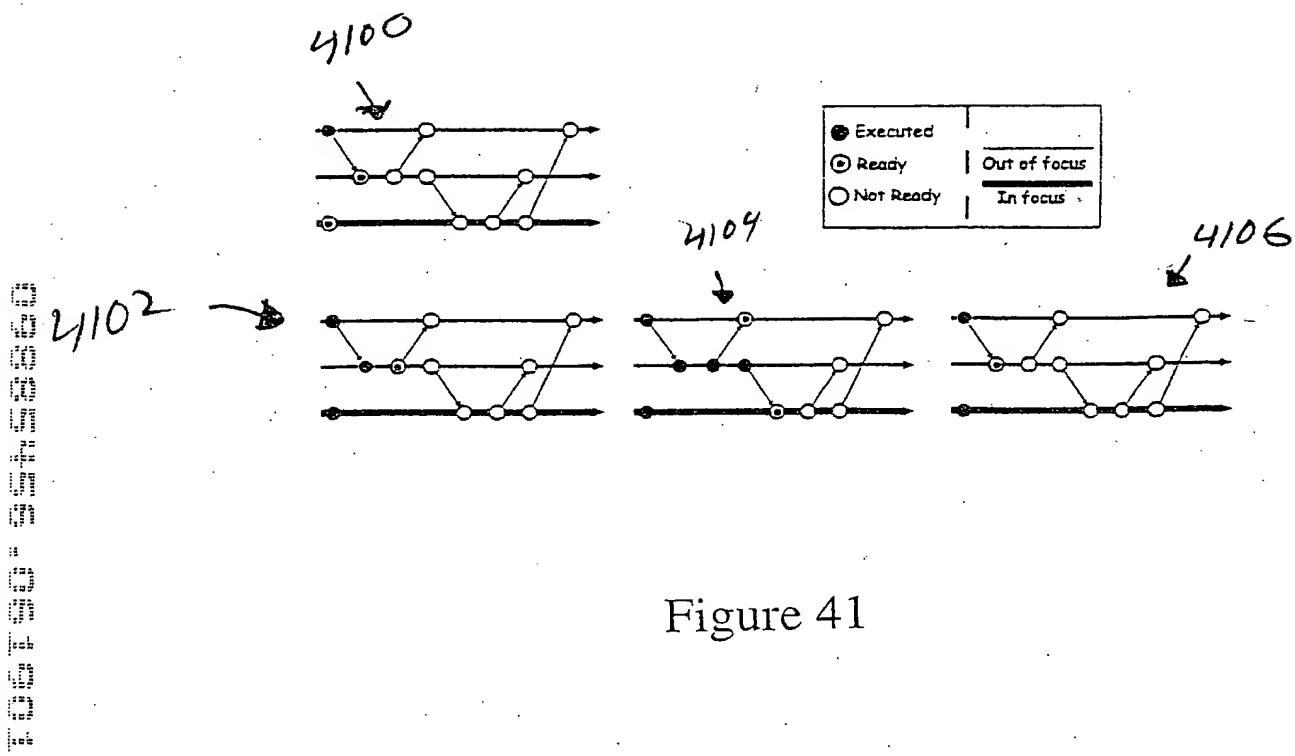


Figure 40



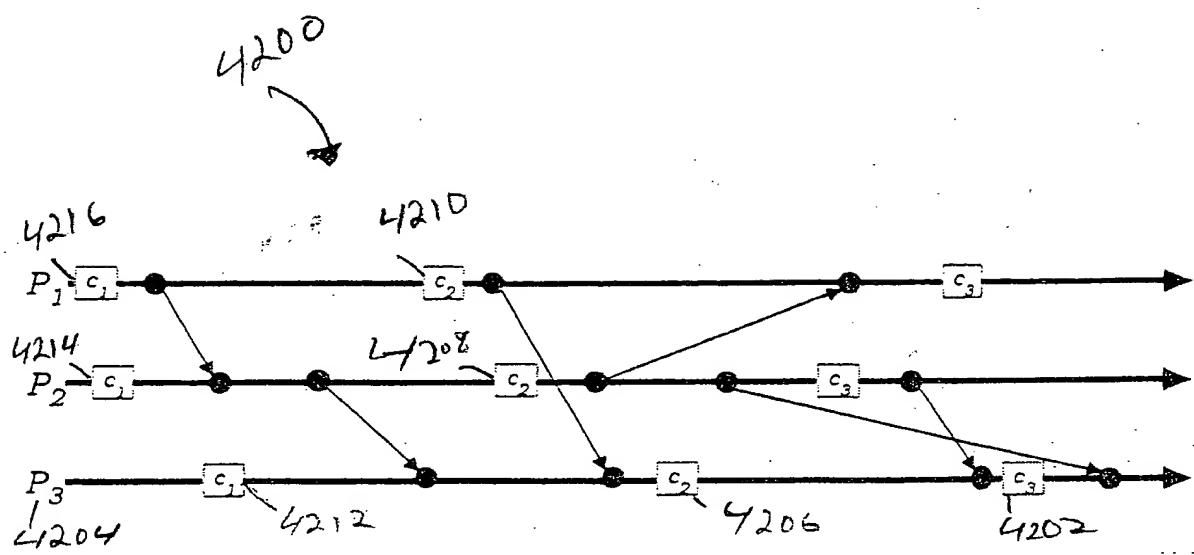


Figure 42

100780 "Software 88600

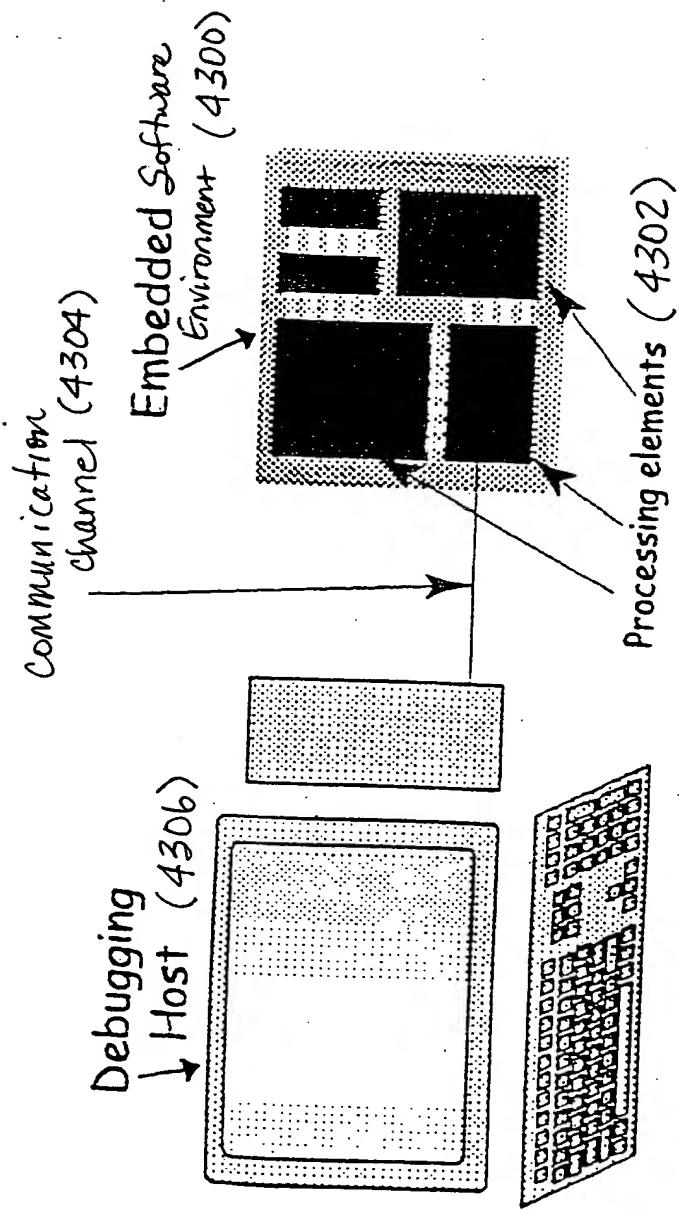


FIG. 43

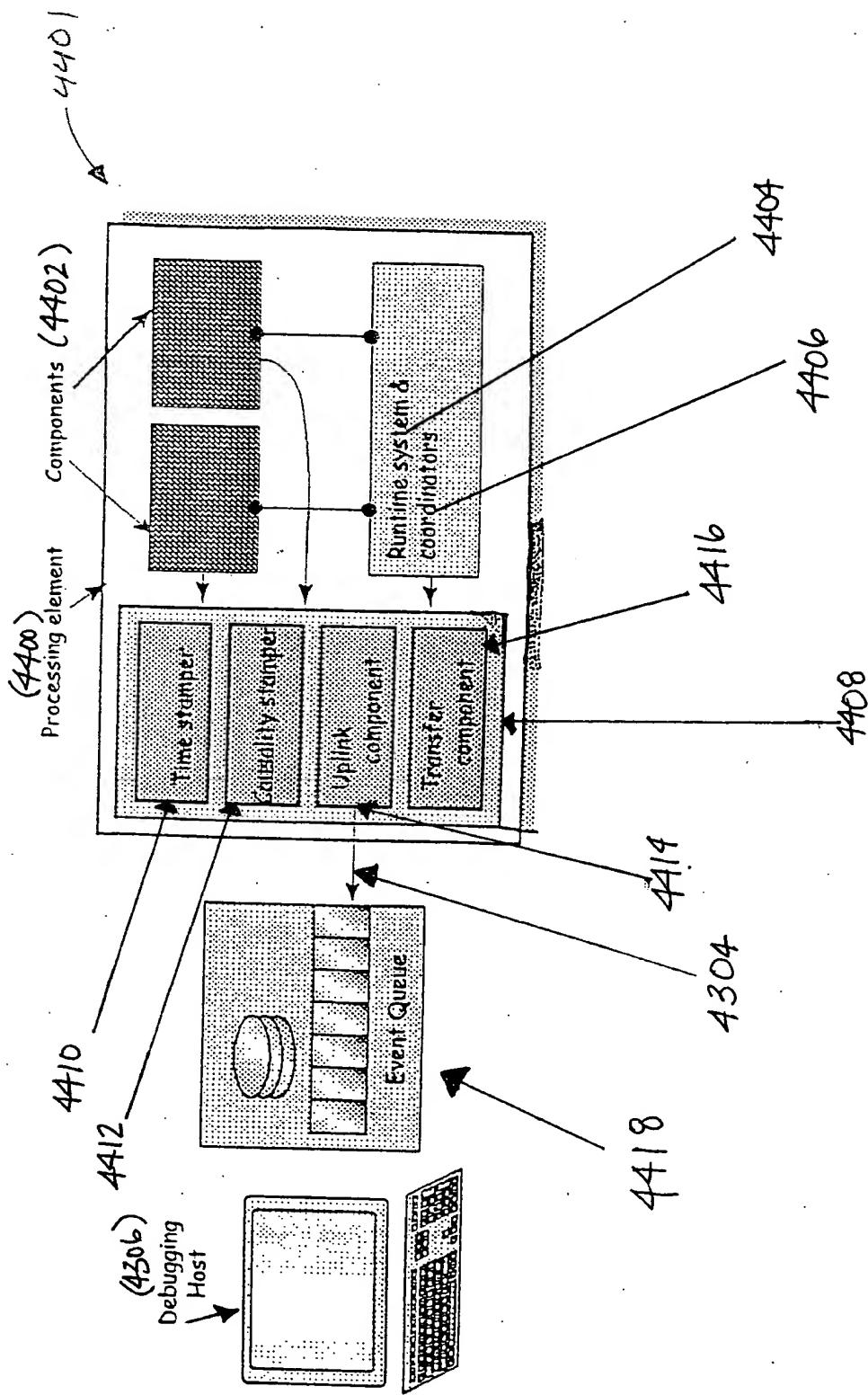


FIG. 44

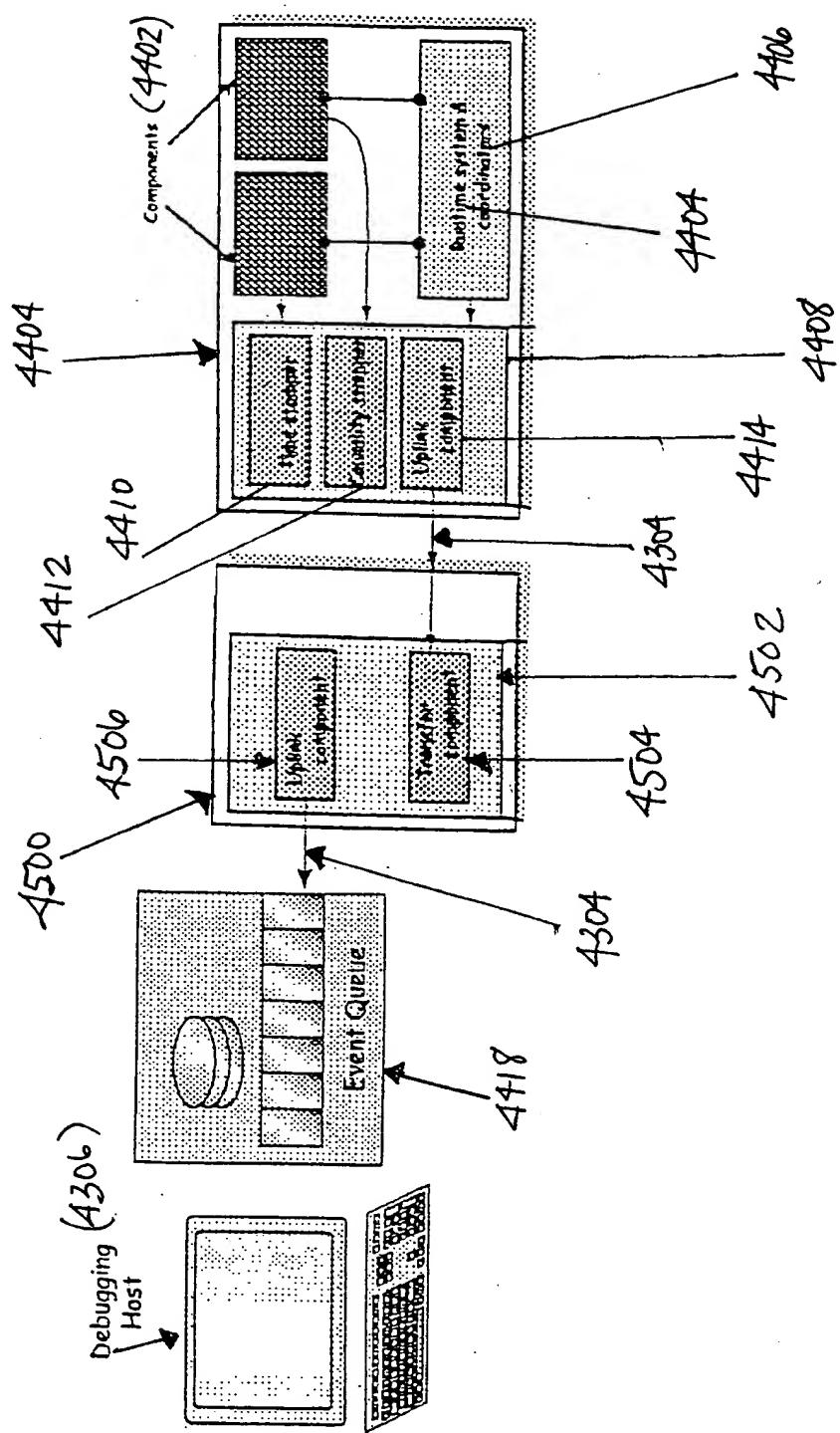


FIG. 45

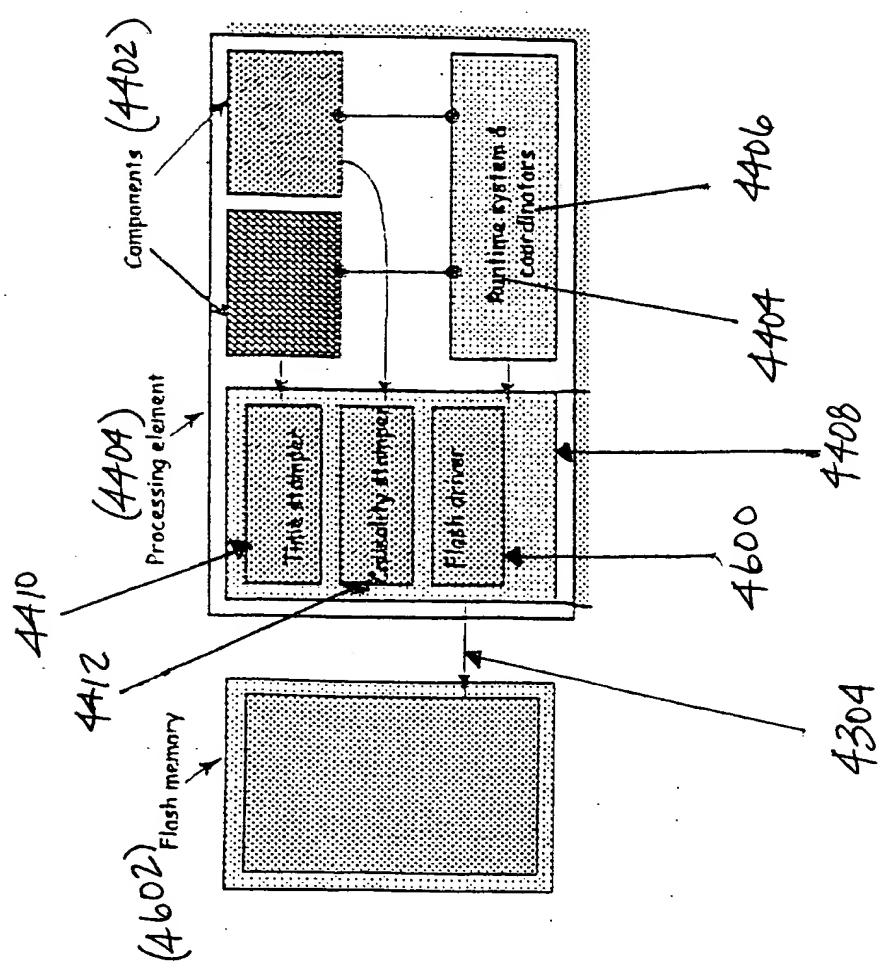
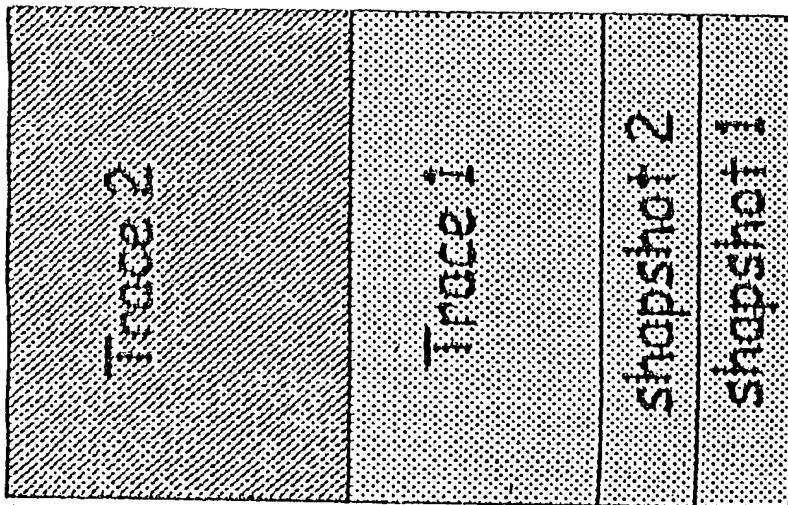
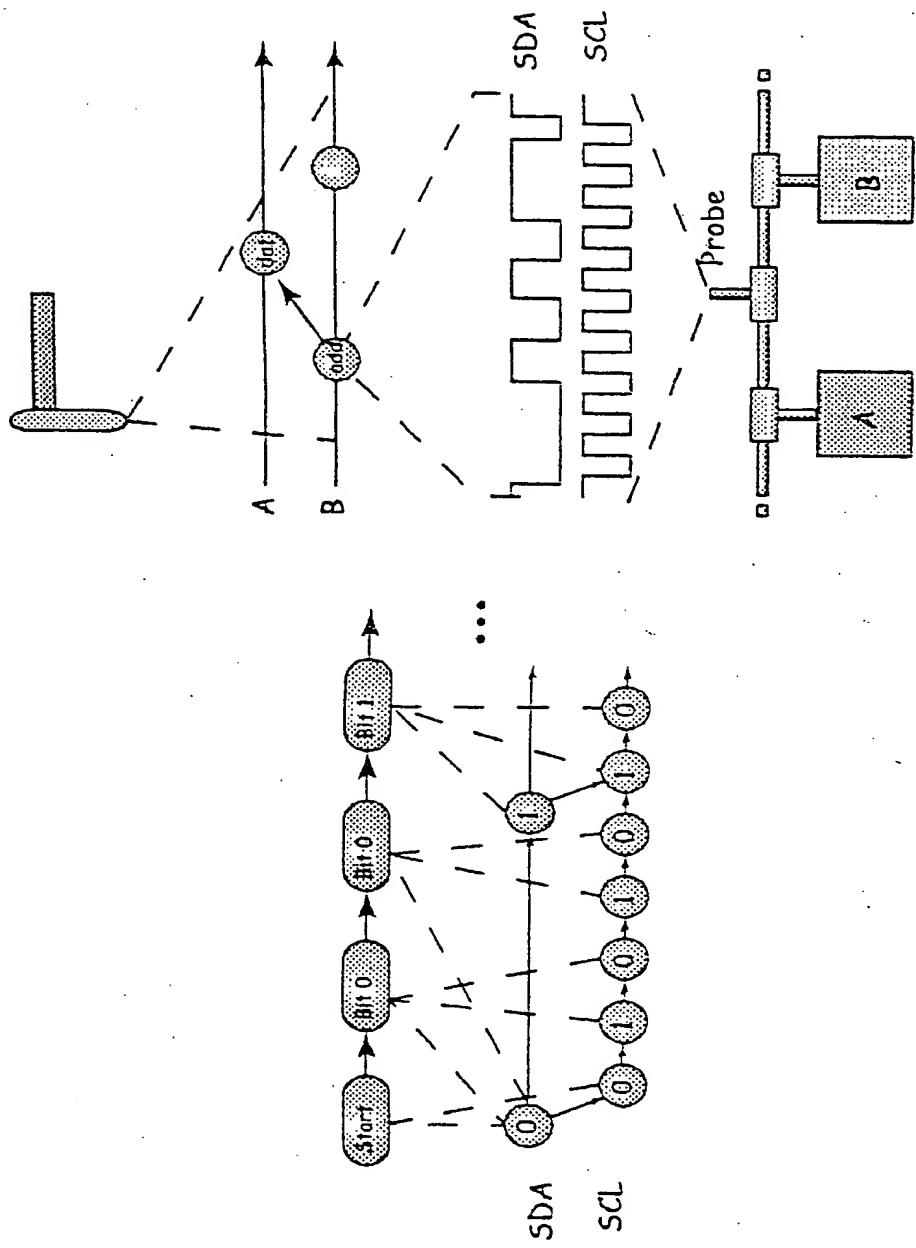


FIG. 46

FIG. 47



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Primitive recognition of first two bits      Multiple representations

FIG. 48